



***AXIOMTEK***

## **CAPA13R**

**AMD® RYZEN™ Embedded  
V1605B/V1807B 3.5" Board**

**User's Manual**



## **Disclaimers**

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## **CAUTION**

If you replace wrong batteries, it causes the danger of explosion. It is recommended by the manufacturer that you follow the manufacturer's instructions to only replace the same or equivalent type of battery, and dispose of used ones.

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**January 2020, Version A1**

**Printed in Taiwan**

## **ESD Precautions**

Computer boards have integrated circuits sensitive to static electricity. To prevent chipsets from electrostatic discharge damage, please take care of the following jobs with precautions:

- Do not remove boards or integrated circuits from their anti-static packaging until you are ready to install them.
- Before holding the board or integrated circuit, touch an unpainted portion of the system unit chassis for a few seconds. It discharges static electricity from your body.
- Wear a wrist-grounding strap, available from most electronic component stores, when handling boards and components.

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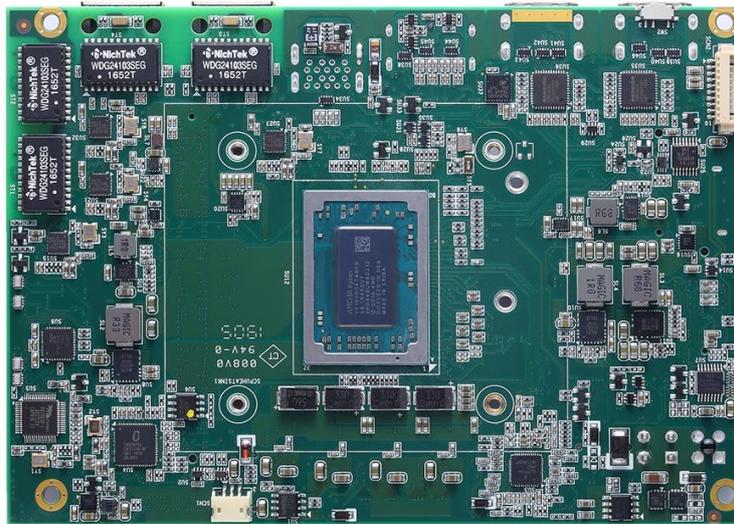
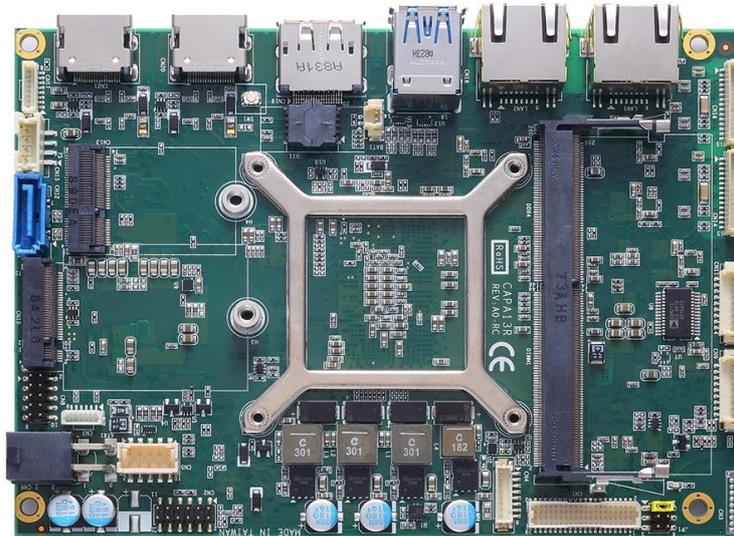
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# Chapter 1

## Introduction



The CAPA13R, a 3.5" board, supports AMD® RYZEN® Embedded V1605B/V1807B processor. It delivers outstanding system performance through high-bandwidth interfaces, multiple I/O functions for interactive applications and various embedded computing solutions.

The CAPA13R has one 260-pin unbuffered SO-DIMM socket for single channel DDR4 2400/3200 MHz memory with maximum capacity up to 16GB. There are four Gigabit/Fast Ethernet ports, one SATA port with transfer rate up to 6Gb/s, two USB 3.1 Gen 2 super speed compliant, two USB 2.0 high speed compliant, and HD audio link that can achieve the best stability and reliability for industrial applications. Additionally, it provides you with unique embedded features, such as two serial ports (one RS-232/422/485, one RS-232) and 3.5" form factor that applies an extensive array of PC peripherals.

## 1.1 Features

- AMD® RYZEN™ quad core V1807B (3.35GHz) and V1605B (2.0GHz)
- 1 DDR4 SO-DIMM supports up to 16 GB memory capacity
- 2 USB 3.1 Gen 2 ports
- 2 USB 2.0 ports
- 2 COM ports
- 4 GbE LAN
- 1 M.2 Key B
- 1 M.2 Key E
- +12V only DC-in supported

## 1.2 Specifications

- **CPU**
  - AMD® RYZEN™ quad core V1807B 3.35GHz.
  - AMD® RYZEN™ quad core V1605B 2.0GHz.
- **Thermal Solution**
  - Active.
- **Operating Temperature**
  - -20°C--60°C.
- **BIOS**
  - American Megatrends Inc. UEFI (Unified Extensible Firmware Interface) BIOS.
  - 64Mbit SPI Flash, DMI, Plug and Play.
  - PXE Ethernet Boot ROM.
- **System Memory**
  - One 260-pin unbuffered DDR4 SO-DIMM socket.
  - Maximum up to 16GB DDR4 2400MHz memory for V1605B.
  - Maximum up to 16GB DDR4 3200MHz memory for V1807B.
- **Onboard Multi I/O**
  - Controller: Fintek F81803.
  - Two serial ports:
    - COM1 supports RS-232/422/485 by BIOS selecting.
    - COM2 supports RS-232 only.
- **Serial ATA**
  - One SATA-600 connector.
  - One M.2 Key B connector in 22x42
- **USB Interface**
  - Two USB ports with fuse protection and complies with USB Spec. Rev. 3.1 Gen 2 in Type A connector.
  - Two USB ports with fuse protection and complies with USB Spec. Rev. 2.0 in 2x5-pin wafer connector.
- **Display**
  - Two HDMI with resolution max. up to 3840x2160 @30Hz.
  - One DisplayPort supports DP++ with max. resolution 4096x2160 @60Hz.
  - One 2x20-pin connector for 18/24-bit single/dual channel LVDS with one 8-pin inverter connector. LVDS resolution is up to 1920x1200 in 24-bit dual channels.

- **Watchdog Timer**
  - 1~255 seconds or minutes; up to 255 levels.
- **Ethernet**
  - Four LAN ports with Realtek RTL8111G support 1000/100/10Mbps Gigabit/Fast Ethernet with Wake-on-LAN and PXE Boot ROM.
- **Audio**
  - HD audio link without codec.
- **Expansion Interface**
  - One M.2 Key E connector in 22x30.
- **Power Input**
  - One 2x2-pin connector.
  - +12V DC-in only.
  - AT auto power on function supported.
- **Power Management**
  - ACPI (Advanced Configuration and Power Interface).
- **Form Factor**
  - 3.5" form factor.



Note

*All specifications and images are subject to change without notice.*

## 1.3 Utilities

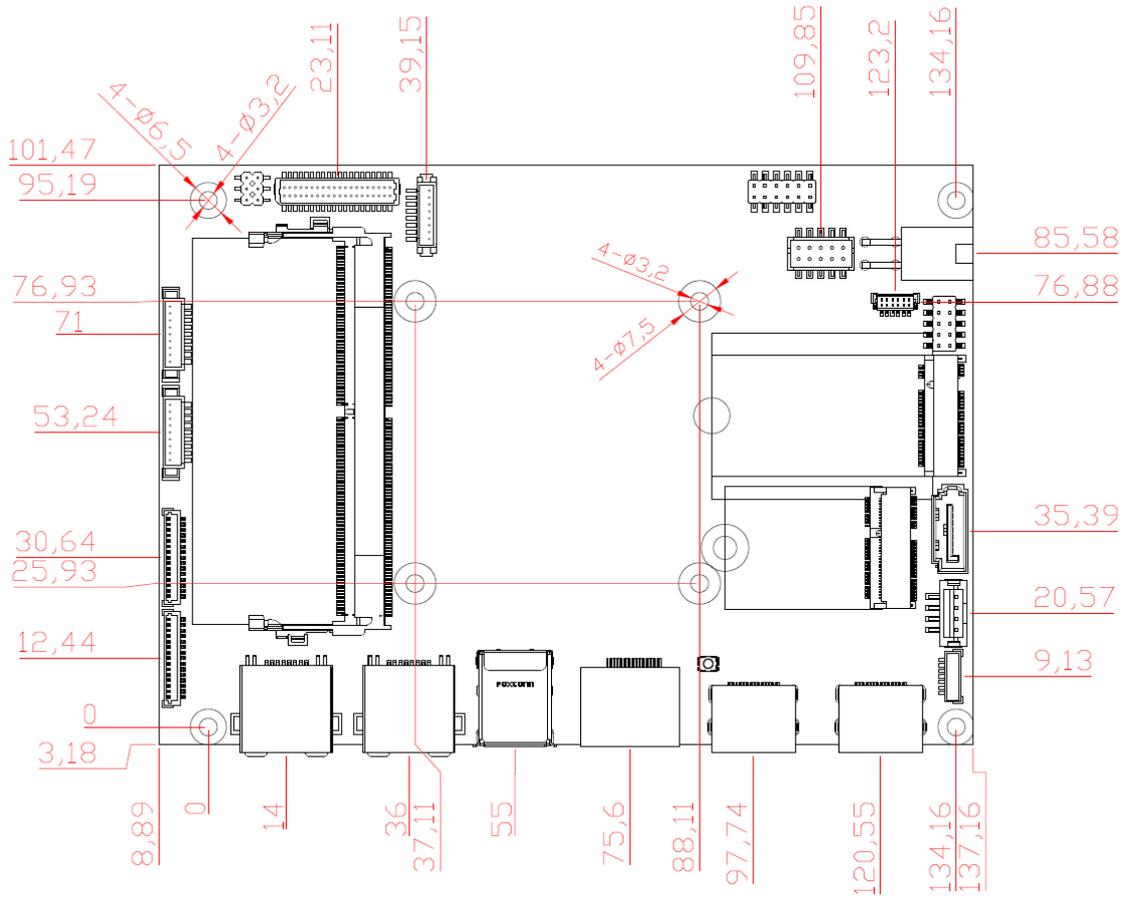
- Graphics driver
- Ethernet driver

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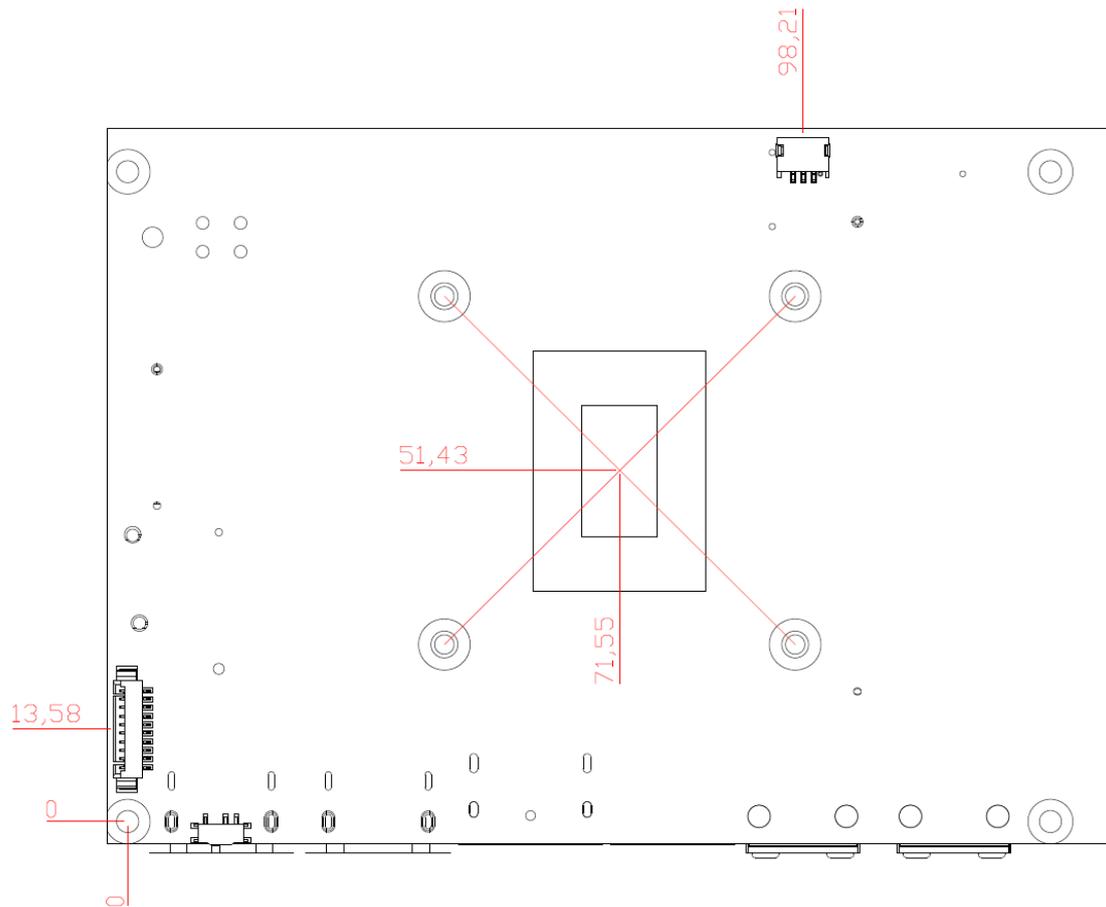
# Chapter 2

## Board and Pin Assignments

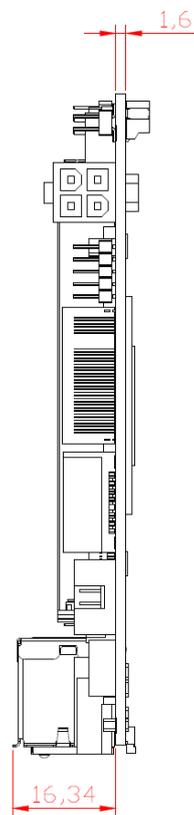
### 2.1 Board Dimensions and Fixing Holes



Top View

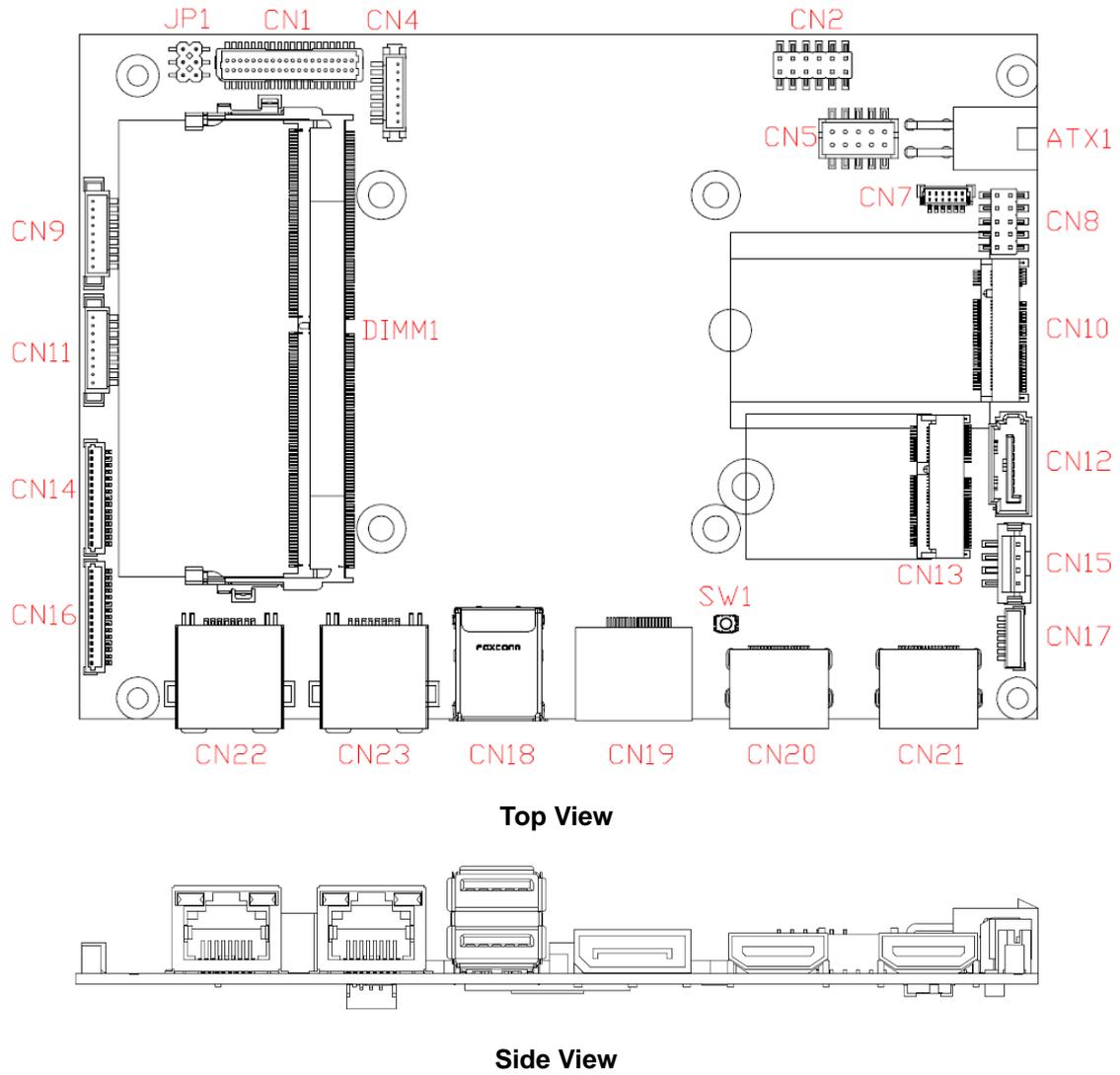


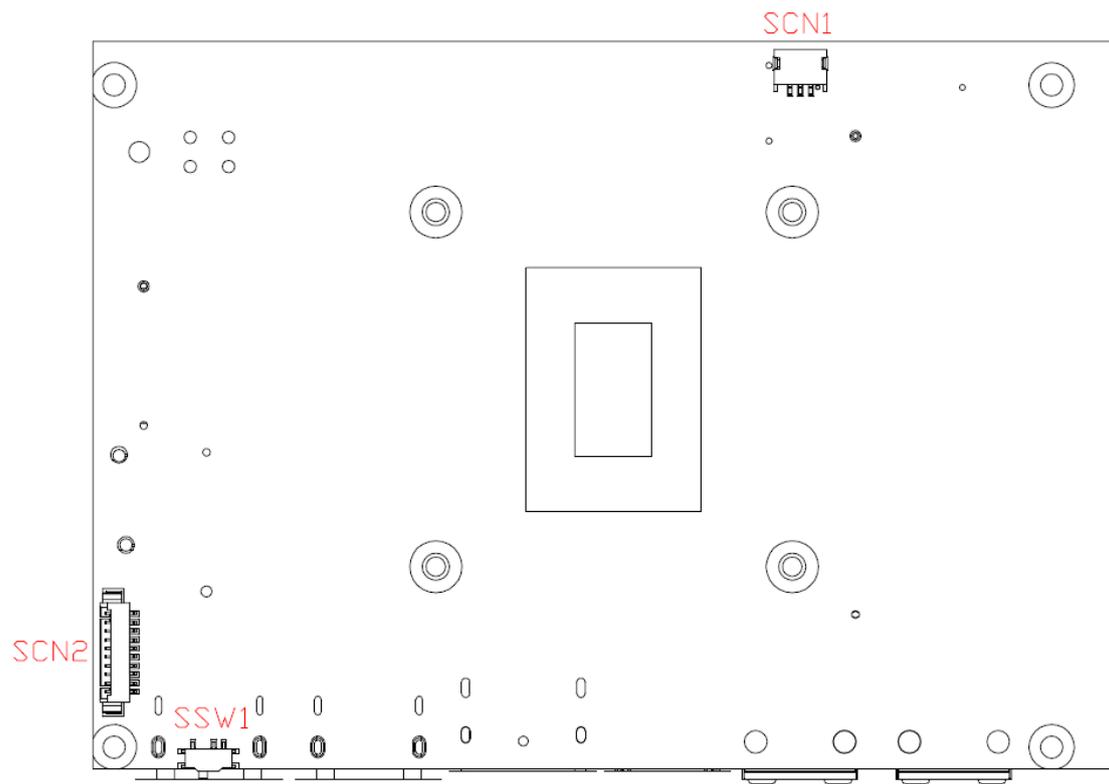
Bottom View



Side View

## 2.2 Board Layout

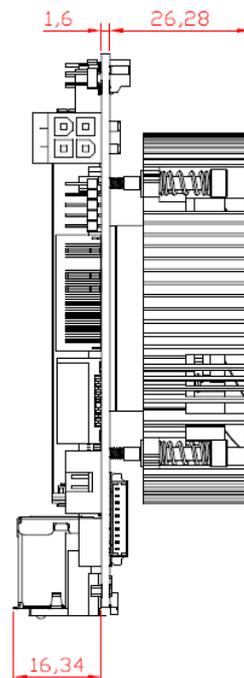
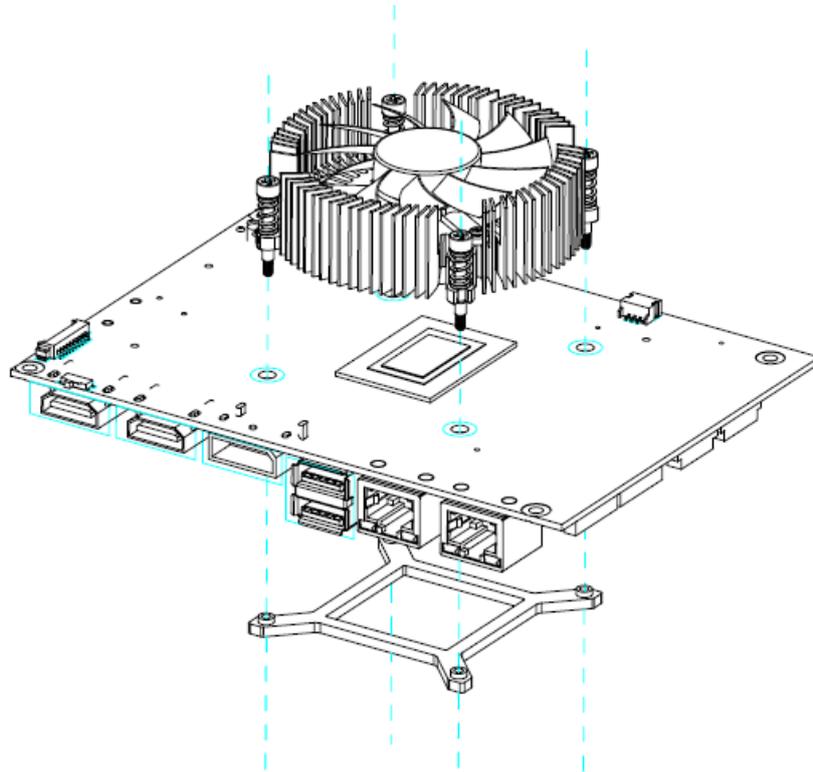




**Bottom View**

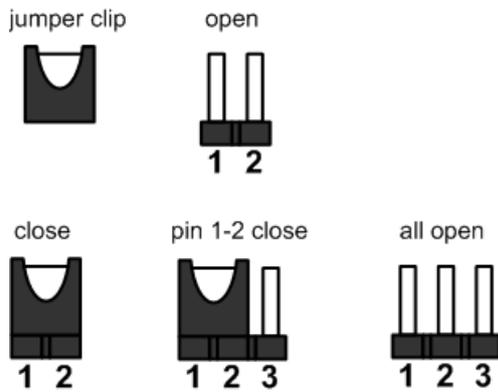
## 2.3 Installing Cooling Fan

Image below illustrates how to install cooling fan on CAPA13R.



## 2.4 Jumper and Switch Settings

Jumper is a small component consisting of jumper clip and jumper pins. Install jumper clip on 2 jumper pins to close. And remove jumper clip from 2 jumper pins to open. Below illustration shows how to set up jumper.



Properly configure jumper and switch settings on the CAPA13R to meet your application purpose. Below you can find a summary table of jumper, switch and onboard default settings.



Note

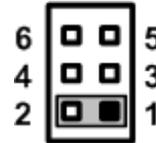
**Once the default jumper or switch setting needs to be changed, please do it under power-off condition.**

Jumper and Switch	Description	Setting
JP1	LVDS Voltage Selection Default: +3.3V Level	1-2 Close
SW1	Restore BIOS Optimal Defaults Default: Normal Operation	Release
SSW1	Auto Power On Default: Disable	1-2 Close

### 2.4.1 LVDS Voltage Selection (JP1)

This is a 2x3-pin (pitch=2.0mm) jumper. The board supports voltage selection for flat panel displays. Use JP1 to set LVDS connector (CN1) pin 1~6 VCCM to +3.3V, +5V or +12V voltage level. To prevent hardware damage, before connecting please make sure that input voltage of the flat panel is correct.

Function	Setting
+3.3V level (Default)	1-2 close
+5V level	2-4 close
	Or 4-6 close
+12V level	5-6 close



### 2.4.2 Restore BIOS Optimal Defaults (SW1)

Use SW1 to clear CMOS. Press the tact switch for at least 1 second to restore BIOS optimal defaults.

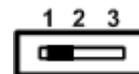
Function	Setting
Normal (Default)	Release
Restore BIOS optimal defaults	Press



### 2.4.3 Auto Power On (SSW1)

If SSW1 is enabled for power input, the system will be automatically power on without pressing soft power button. If SSW1 is disabled for power input, it is necessary to manually press soft power button to power on the system.

Function	Setting
Disable auto power on (Default)	1-2 close
Enable auto power on	2-3 close



## 2.5 Connectors

Signals go to other parts of the system through connectors. Loose or improper connection might cause problems, please make sure all connectors are properly and firmly connected. Here is a summary table of connectors on the hardware.

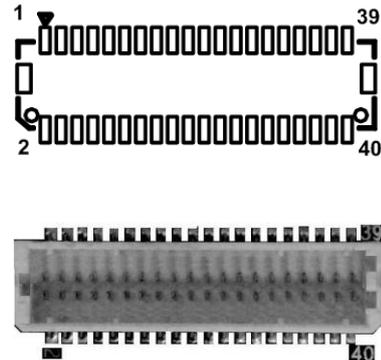
Connector	Description
CN1	LVDS Connector
CN2	Front Panel Connector
CN4	Inverter Connector
CN5	USB 2.0 Wafer Connector
CN7	SIM Card Wafer Connector
CN8	Digital I/O Connector
CN9, CN11	COM1 and COM2 Wafer Connectors
CN10	M.2 2242/3042 Key B Connector
CN12	SATA Connector
CN13	M.2 2230 Key E Connector
CN14, CN16	Ethernet Port 4 and Port 3 Wafer Connectors
CN15	SATA Power Connector
CN17	Audio Wafer Connector
CN18	USB 3.1 Port 1 and 2
CN19	DisplayPort Connector
CN20, CN21	HDMI Connectors
CN22, CN23	Ethernet Port 1 and 2
ATX1	ATX Power Connector
SCN1	Fan Connector
SCN2	DC +5V/15W Power Output Connector
DIMM1	DDR4 SO-DIMM Connector

### 2.5.1 LVDS Connector (CN1)

This board has one 2x20-pin (pitch=1.0mm) connector for LVDS LCD interface. It is strongly recommended to use the matching connector, JST SHDR-40VS-B, for LVDS interface. The Pin 1~6 VCCM can be set to +3.3V, +5V or +12V using JP1 (see section 2.4.1). **Please note that when making LVDS cable, the pin 2 of JST SHDR-40VS-B connector should match the pin 1 of CN1.**

#### 18-bit single channel

Pin	Signal	Pin	Signal
1	VCCM	2	VCCM
3	VCCM	4	VCCM
5	VCCM	6	VCCM
7	N.C	8	N.C
9	GND	10	GND
11	N.C	12	N.C
13	N.C	14	N.C
15	GND	16	GND
17	N.C	18	N.C
19	N.C	20	N.C
21	GND	22	GND
23	Channel A D0-	24	N.C
25	Channel A D0+	26	N.C
27	GND	28	GND
29	Channel A D1-	30	N.C
31	Channel A D1+	32	N.C
33	GND	34	GND
35	Channel A D2-	36	Channel A CLK-
37	Channel A D2+	38	Channel A CLK+
39	GND	40	GND



**24-bit single channel**

Pin	Signal	Pin	Signal
1	VCCM	2	VCCM
3	VCCM	4	VCCM
5	VCCM	6	VCCM
7	N.C	8	N.C
9	GND	10	GND
11	N.C	12	N.C
13	N.C	14	N.C
15	GND	16	GND
17	N.C	18	N.C
19	N.C	20	N.C
21	GND	22	GND
23	Channel A D0-	24	N.C
25	Channel A D0+	26	N.C
27	GND	28	GND
29	Channel A D1-	30	Channel A D3-
31	Channel A D1+	32	Channel A D3+
33	GND	34	GND
35	Channel A D2-	36	Channel A CLK-
37	Channel A D2+	38	Channel A CLK+
39	GND	40	GND

**18-bit dual channel**

Pin	Signal	Pin	Signal
1	VCCM	2	VCCM
3	VCCM	4	VCCM
5	VCCM	6	VCCM
7	N.C	8	N.C
9	GND	10	GND
11	N.C	12	Channel B D0-
13	N.C	14	Channel B D0+
15	GND	16	GND
17	Channel B CLK-	18	Channel B D1-
19	Channel B CLK+	20	Channel B D1+
21	GND	22	GND
23	Channel A D0-	24	Channel B D2-
25	Channel A D0+	26	Channel B D2+
27	GND	28	GND
29	Channel A D1-	30	N.C
31	Channel A D1+	32	N.C
33	GND	34	GND
35	Channel A D2-	36	Channel A CLK-
37	Channel A D2+	38	Channel A CLK+
39	GND	40	GND

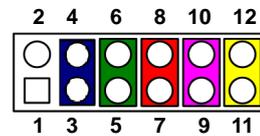
**24-bit dual channel**

Pin	Signal	Pin	Signal
1	VCCM	2	VCCM
3	VCCM	4	VCCM
5	VCCM	6	VCCM
7	N.C	8	N.C
9	GND	10	GND
11	Channel B D3-	12	Channel B D0-
13	Channel B D3+	14	Channel B D0+
15	GND	16	GND
17	Channel B CLK-	18	Channel B D1-
19	Channel B CLK+	20	Channel B D1+
21	GND	22	GND
23	Channel A D0-	24	Channel B D2-
25	Channel A D0+	26	Channel B D2+
27	GND	28	GND
29	Channel A D1-	30	Channel A D3-
31	Channel A D1+	32	Channel A D3+
33	GND	34	GND
35	Channel A D2-	36	Channel A CLK-
37	Channel A D2+	38	Channel A CLK+
39	GND	40	GND

## 2.5.2 Front Panel Connector (CN2)

This is a 2x6-pin header (pitch=2.0mm) for front panel interface.

Pin	Signal	Pin	Signal
1	BUZZER-	2	BUZZER+
3	GND	4	PWR_PSON
5	PWRLED-	6	PWRLED+
7	PWRSW-	8	PWRSW+
9	HW RST-	10	HW RST+
11	HDDLED-	12	HDDLED+



### Internal Buzzer

Pin 1(-) and 2(+) connect the internal buzzer cable.

### Power Status (PS-ON)

Pin 4 and pin 3 are PS-ON signal which are connected to know the power status of this board.

### Power LED

Pin 6 connects anode (+) of LED and pin 5 connects cathode (-) of LED. The power LED lights up when the system is powered on.

### Power On/Off Button

Pin 8 and 7 connect the power button on front panel to CPU board, which allows users to turn on or off power.

### System Reset Switch

Pin 10 and 9 connect the case-mounted reset switch that reboots your computer without turning off the power switch. It is a better way to reboot your system for a longer life of system power supply.

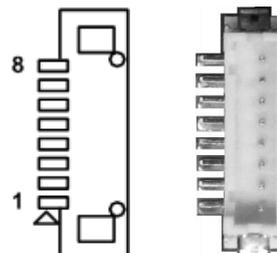
### HDD Activity LED

This connection is linked to hard drive activity LED on the control panel. LED flashes when HDD is being accessed. Pin 12 and 11 connect the hard disk drive to the front panel HDD LED, pin 11 is assigned as cathode(-) and pin 12 is assigned as anode(+).

## 2.5.3 Inverter Connector (CN4)

This is an 8-pin (pitch=1.25mm) connector which is compliant with Hirose DF13-8P-1.25V for inverter interface. We strongly recommend you to use the matching connector, DF13-8S-1.25C, to avoid malfunction.

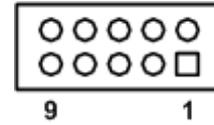
Pin	Signal
1	VBL1 (+12V level)
2	VBL1 (+12V level)
3	VBL2 (+5V level)
4	VBL_ENABLE
5	GND
6	GND
7	GND
8	VBL Brightness Control



### 2.5.4 USB 2.0 Wafer Connector (CN5)

This is a 2x5-pin (pitch=2mm) wafer connector, which is compliant with Hirose DF11-XDP-2DSA, for installing versatile USB 2.0 compliant interface peripherals.

Pin	Signal	Pin	Signal
1	USB VCC (+5V_SBY)	2	USB VCC (+5V_SBY)
3	USB_1_DM0	4	USB_1_DM1
5	USB_1_DP0	6	USB_1_DP1
7	GND	8	GND
9	GND	10	GND

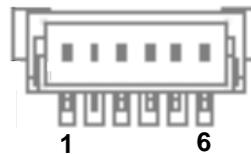


### 2.5.5 SIM Card Wafer Connector (CN7)

The CN7 is a 6-pin (pitch=1.0mm) wafer connector, which is compliant with JST B6B-PH-K-S, for SIM Card interface. AX93A19 is suggested to use for CN7 to have SIM card slot.

In order to work properly, the SIM card must be used together with 3G/4G LTE module in M.2 Key B connector/CN10. It is mainly used in 3G/4G LTE cellular network application.

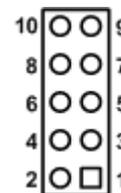
Pin	Signal
1	PWR
2	RESET
3	CLK
4	DAT
5	VPP
6	GND



### 2.5.6 Digital I/O Connector (CN8)

This is a 2x5-pin (pitch=2.0mm) connector. The board is equipped with an 8-bit digital I/O that meets requirements for a system customary automation control. The digital I/O can be configured to control cash drawers and sense warning signals from an Uninterrupted Power System (UPS), or perform store security control. You may use software programming to control these digital signals, please refer to Appendix B.

Pin	Signal	Pin	Signal
1	Digital Input Output 0 (Default: Output)	2	Digital Input Output 7 (Default: Input)
3	Digital Input Output 1 (Default: Output)	4	Digital Input Output 6 (Default: Input)
5	Digital Input Output 2 (Default: Output)	6	Digital Input Output 5 (Default: Input)
7	Digital Input Output 3 (Default: Output)	8	Digital Input Output 4 (Default: Input)
9	+5V level	10	GND

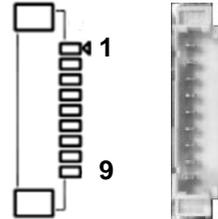


## 2.5.7 COM1 and COM2 Wafer Connectors (CN9 and CN11)

These are 9-pin (pitch=1.25mm) connectors which are compliant with Molex 53047-0910. The pin assignments of RS-232/RS-422/RS-485 are listed in table below. If you need COM1 port to support RS-422 or RS-485, please refer to BIOS setting in section 4.4.

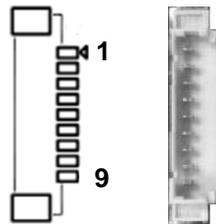
### CN9 (for COM1):

Pin	RS-232	RS-422	RS-485
1	DCD	TX-	Data-
2	DSR	No use	No use
3	RXD	TX+	Data+
4	RTS	No use	No use
5	TXD	RX+	No use
6	CTS	No use	No use
7	DTR	RX-	No use
8	RI	No use	No use
9	GND	No use	No use



### CN11 (for COM2):

Pin	RS-232
1	DCD
2	DSR
3	RXD
4	RTS
5	TXD
6	CTS
7	DTR
8	RI
9	GND



### 2.5.8 M.2 2242/3042 Key B Connector (CN10)

The CN10 is a M.2 Key B connector. It is suggested to install the M.2 storage module via SATA with 22mm width and 42mm length or the M.2 cellular module via USB 2.0 with 30mm width and 42mm length. It also supports the M.2 storage module via PCIe x2 by BOM option with hardware change.

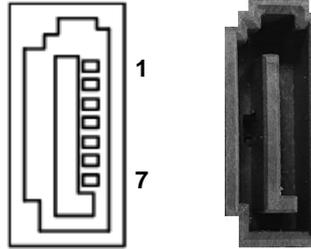
Pin	Signal	Pin	Signal
1	CONFIG_3	2	+3.3V_SBY
3	GND	4	+3.3V_SBY
5	GND	6	Full Card PWR OFF
7	USB_0_DP0	8	W_DISABLE#
9	USB_0_DM0	10	NC
11	GND	12	<b>Key B</b>
13	<b>Key B</b>	14	
15		16	
17		18	
19		20	NC
21	CONFIG_0	22	NC
23	GPIO11(+1.8V)	24	NC
25	NC	26	NC
27	GND	28	NC
29	PCIE_RXN4	30	SIM_RST
31	PCIE_RXP4	32	SIM_CLK
33	GND	34	SIM_DATA
35	PCIE_TXN4	36	SIM_PWR
37	PCIE_TXP4	38	NC
39	GND	40	SMB_CLK_SBY(+1.8V)
41	SATA1_RXP/ PCIE_RXN5	42	SMB_DATA_SBY(+1.8V)
43	SATA1_RXN/ PCIE_RXP5	44	NC
45	GND	46	NC
47	SATA1_TXN/ PCIE_TXN5	48	NC
49	SATA1_TXP/ PCIE_TXP5	50	PERST#
51	GND	52	CLK_REQ4
53	PCIE_CLKN4	54	PEWAKE#
55	PCIE_CLKP4	56	NC
57	GND	58	NC
59	NC	60	NC
61	NC	62	NC
63	NC	64	NC
65	NC	66	NC
67	PLTRST(+1.8V)	68	SUSCLK
69	CONFIG_1	70	+3.3V_SBY
71	GND	72	+3.3V_SBY
73	GND	74	+3.3V_SBY
75	CONFIG_2		



### 2.5.9 SATA Connector (CN12)

This Serial Advanced Technology Attachment (Serial ATA or SATA) connector is for high-speed SATA interface. It is a computer bus interface for connecting to devices such as hard disk drive.

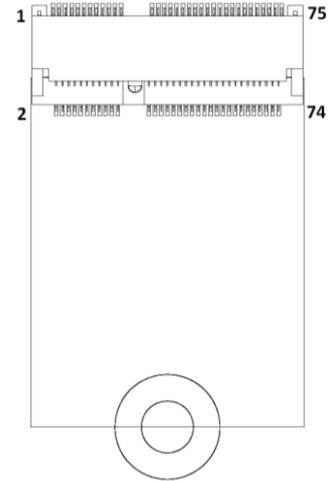
Pin	Signal
1	GND
2	SATA_TXP0
3	SATA_TXN0
4	GND
5	SATA_RXN0
6	SATA_RXP0
7	GND



### 2.5.10 M.2 2230 Key E Connector (CN13)

The CN13 is a M.2 2230 Key E connector. It is suggested to install the M.2 wireless module via PCIe x1 and USB 2.0 with 22mm width and 30mm length.

Pin	Signal	Pin	Signal
1	GND	2	+3.3V_SBY
3	USB_0_DP3	4	+3.3V_SBY
5	USB_0_DM3	6	NC
7	GND	8	NC
9	NC	10	NC
11	NC	12	NC
13	NC	14	NC
15	NC	16	NC
17	NC	18	GND
19	NC	20	NC
21	NC	22	NC
23	NC	24	<b>Key E</b>
25	<b>Key E</b>	26	
27		28	
29		30	
31		32	
33		GND	34
35	PCIE_TXP0	36	NC
37	PCIE_TXN0	38	NC
39	GND	40	NC
41	PCIE_RXP0	42	NC
43	PCIE_RXN0	44	NC
45	GND	46	NC
47	PCIE_CLKP2	48	NC
49	PCIE_CLKN2	50	SUSCLK(+3.3V)
51	GND	52	PERST#(+3.3V)
53	CLK_REQ2	54	W_DIS2#(+3.3V)
55	PEWAKE0#	56	W_DI12#(+3.3V)
57	GND	58	SMB_DATA_SBY(+3.3V)
59	NC	60	SMB_CLK_SBY(+3.3V)
61	NC	62	NC
63	GND	64	NC
65	NC	66	NC
67	NC	68	NC
69	GND	70	NC
71	NC	72	+3.3V_SBY
73	NC	74	+3.3V_SBY
75	GND		

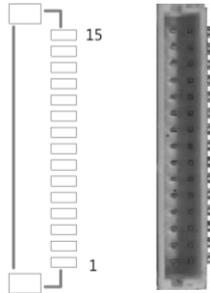


### 2.5.11 Ethernet Wafer Connectors (CN14 for LAN4 and CN16 for LAN3)

This is a wafer connector which is compliant with JST BM16B-SRSS-TB 15-pin (pitch=1.0mm) for Ethernet port 3 and port 4 interface.

It is suggested to connector AX93287 to have RJ45 connector for Ethernet connection.

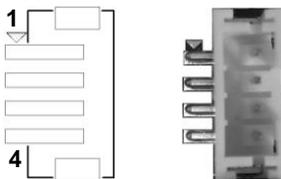
Pin	Signal
1	GND
2	LAN_LINK_ACT
3	LAN_VDD33
4	GND
5	MDI0+
6	MDI0-
7	MDI1+
8	MDI2+
9	MDI2-
10	MDI1-
11	MDI3+
12	MDI3-
13	GND
14	LAN_100_LED
15	LAN_1000_LED



### 2.5.12 SATA Power Connector (CN15)

This is a 4-pin (pitch=2mm) wafer connector, which is compliant with JST B4B-PH-K-S, for SATA power interface.

Pin	Signal
1	+12V
2	GND
3	GND
4	+5V

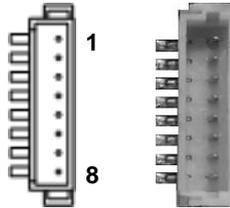


### 2.5.13 Audio Wafer Connector (CN17)

This is an 8-pin (pitch=1.0mm) wafer connector, which is compliant with JST BM08B-SRSS-TB, for HD audio interface.

It is suggested to connect AX93A22 to have Mic in/Line in and Line out.

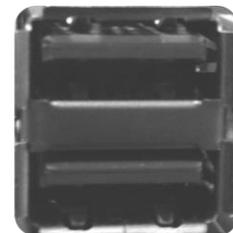
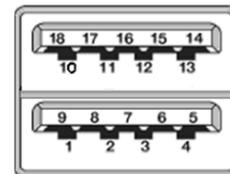
Pin	Signal
1	+5V_SBY
2	AZ_BIT_CLK
3	AZ_SDATA_IN
4	AZ_SDATA_OUT
5	AZ_SYNC
6	AZ_RST#
7	SLP_S3#
8	GND



### 2.5.14 USB 3.1 Port (CN18)

The board comes with two Universal Serial Bus (compliant with USB 3.1 Gen 2 (10Gb/s)) ports on the rear I/O for installing USB peripherals such as keyboard, mouse, scanner, etc.

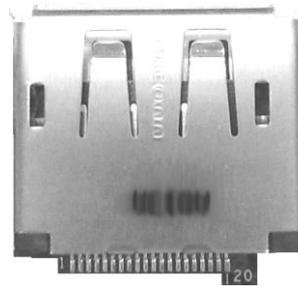
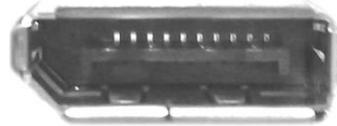
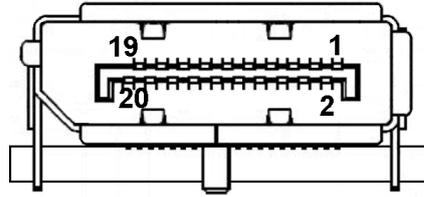
Pin	Signal	Pin	Signal
1	USB_VCC (+5V )	10	USB_VCC (+5V )
2	USB #0_D-	11	USB #1_D-
3	USB #0_D+	12	USB #1_D+
4	GND	13	GND
5	SSRX0-	14	SSRX1-
6	SSRX0+	15	SSRX1+
7	GND	16	GND
8	SSTX0-	17	SSTX1-
9	SSTX0+	18	SSTX1+



### 2.5.15 DisplayPort Connector (CN19)

The DisplayPort interface is available through connector CN19.

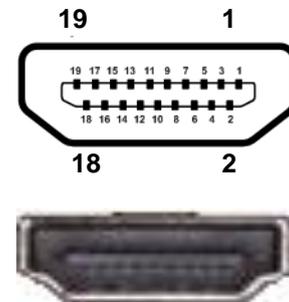
Pin	Signal
1	DP_LANE0
2	GND
3	DP_LANE0#
4	DP_LANE1
5	GND
6	DP_LANE1#
7	DP_LANE2
8	GND
9	DP_LANE2#
10	DP_LANE3
11	GND
12	DP_LANE3#
13	Detect Pin
14	GND
15	DP_AUX
16	GND
17	DP_AUX#
18	DP_HPDE
19	GND
20	+3.3V



### 2.5.16 HDMI Connector (CN20 and CN21)

The HDMI (High-Definition Multimedia Interface) is a compact digital interface which is capable of transmitting high-definition video and high-resolution audio over a single cable. The board comes with two HDMI connectors on the rear I/O.

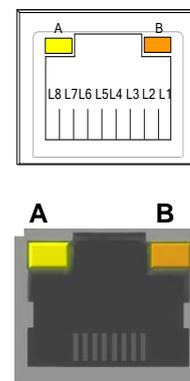
Pin	Signal	Pin	Signal
1	HDMI OUT_DATA2+	2	GND
3	HDMI OUT_DATA2-	4	HDMI OUT_DATA1+
5	GND	6	HDMI OUT_DATA1-
7	HDMI OUT_DATA0+	8	GND
9	HDMI OUT_DATA0-	10	HDMI OUT_Clock+
11	GND	12	HDMI OUT_Clock-
13	N.C.	14	N.C.
15	HDMI OUT_SCL	16	HDMI OUT_SDA
17	GND	18	+5V
19	HDMI_HTPLG		



### 2.5.17 Ethernet Ports (CN22 and CN23)

The board has two RJ-45 Ethernet connectors; CN22 for LAN1 and CN23 for LAN2. Connection can be established by plugging one end of the Ethernet cable into this connector and the other end (phone jack) to a 1000/100/10-Base-T hub.

Pin	1000 Base-T	100/10 Base-T	Description
L1	BI_DA+	TX+	Bidirectional or Transmit Data+
L2	BI_DA-	TX-	Bidirectional or Transmit Data-
L3	BI_DB+	RX+	Bidirectional or Receive Data+
L4	BI_DC+	N.C.	Bidirectional or Not Connected
L5	BI_DC-	N.C.	Bidirectional or Not Connected
L6	BI_DB-	RX-	Bidirectional or Receive Data-
L7	BI_DD+	N.C.	Bidirectional or Not Connected
L8	BI_DD-	N.C.	Bidirectional or Not Connected
A	Active Link LED Off: No link Blinking: Data activity detected		
B	Speed LED 1000: Orange 100/10: Green/OFF		

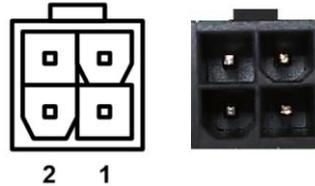


### 2.5.18 ATX Power Connector (ATX1)

Steady and sufficient power can be supplied to all components on the board by connecting the power connector. Please make sure all components and devices are properly installed before connecting the power connector.

The ATX1 is a 4-pin power input interface. Follow the connector orientation to plug the external power supply. Properly press down power supply plug until it completely and firmly fits into this connector. Loose connection may cause system instability.

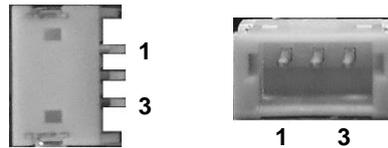
Pin	Signal
1	GND
2	GND
3	+12V
4	+12V



### 2.5.19 Fan Connector (SCN1)

This is a 3-pin (pitch=1.5mm) connector for fan interface.

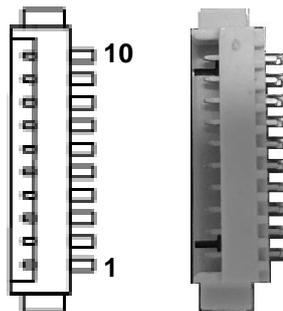
Pin	Signal
1	GND
2	+12V level
3	Fan speed feedback



### 2.5.20 DC +5V/15W Power Output Connector (SCN2)

The SCN2 is a 10-pin (pitch=1.25mm) wafer connector, which is compliant with Molex 53261-054 series, for power output interface.

Pin	Signal
1	+5V
2	+5V
3	+5V
4	+5V
5	N.C
6	N.C
7	GND
8	GND
9	GND
10	GND



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# Chapter 3

## Hardware Description

### 3.1 Microprocessors

The CAPA13R supports AMD Ryzen™ Embedded V1000 series SoC which enables your system to operate under Windows® 10 environments. The system performance depends on the microprocessor. Make sure all correct settings are arranged for the installed microprocessor to prevent the CPU from damages.

### 3.2 BIOS

The CAPA13R uses AMI Plug and Play BIOS with a single 64Mbit SPI Flash.

### 3.3 System Memory

The CAPA13R supports one 260-pin DDR4 SO-DIMM socket. The memory module comes in sizes of 4GB, 8GB and 16GB.

### 3.4 I/O Port Address Map

- ▼  Input/output (IO)
  - >  [0000000000000000 - 000000000000003AF] PCI Express Root Complex
  - >  [000000000000003B0 - 000000000000003DF] PCI Express Root Complex
  - >  [000000000000003E0 - 00000000000000CF7] PCI Express Root Complex
  - >  [00000000000000D00 - 00000000000000FFFF] PCI Express Root Complex

### 3.5 Interrupt Controller (IRQ) Map

The interrupt controller (IRQ) mapping list is shown as follows:

 Interrupt request (IRQ)		 (ISA) 0x00000053 (83)	Microsoft ACPI-Compliant System
 (ISA) 0x00000000 (00)	High precision event timer	 (ISA) 0x00000054 (84)	Microsoft ACPI-Compliant System
 (ISA) 0x00000000 (00)	System timer	 (ISA) 0x00000055 (85)	Microsoft ACPI-Compliant System
 (ISA) 0x00000001 (01)	Standard PS/2 Keyboard	 (ISA) 0x00000056 (86)	Microsoft ACPI-Compliant System
 (ISA) 0x00000003 (03)	AMD UART Controller	 (ISA) 0x00000057 (87)	Microsoft ACPI-Compliant System
 (ISA) 0x00000003 (03)	Communications Port (COM2)	 (ISA) 0x00000058 (88)	Microsoft ACPI-Compliant System
 (ISA) 0x00000004 (04)	AMD UART Controller	 (ISA) 0x00000059 (89)	Microsoft ACPI-Compliant System
 (ISA) 0x00000004 (04)	Communications Port (COM1)	 (ISA) 0x0000005A (90)	Microsoft ACPI-Compliant System
 (ISA) 0x00000005 (05)	Communications Port (COM1)	 (ISA) 0x0000005B (91)	Microsoft ACPI-Compliant System
 (ISA) 0x00000006 (06)	Communications Port (COM2)	 (ISA) 0x0000005C (92)	Microsoft ACPI-Compliant System
 (ISA) 0x00000007 (07)	AMD GPIO Controller	 (ISA) 0x0000005D (93)	Microsoft ACPI-Compliant System
 (ISA) 0x00000008 (08)	High precision event timer	 (ISA) 0x0000005E (94)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000A (10)	PCI Express Downstream Switch Port	 (ISA) 0x0000005F (95)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000A (10)	PCI Express Downstream Switch Port	 (ISA) 0x00000060 (96)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000A (10)	PCI Express Downstream Switch Port	 (ISA) 0x00000061 (97)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000A (10)	Realtek PCIe GbE Family Controller #5	 (ISA) 0x00000062 (98)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000A (10)	Realtek PCIe GbE Family Controller #7	 (ISA) 0x00000063 (99)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000B (11)	AMD Radeon(TM) Vega 11 Graphics	 (ISA) 0x00000064 (100)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000B (11)	PCI Express Downstream Switch Port	 (ISA) 0x00000065 (101)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000B (11)	PCI Express Upstream Switch Port	 (ISA) 0x00000066 (102)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000B (11)	Realtek PCIe GbE Family Controller #6	 (ISA) 0x00000067 (103)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000B (11)	Realtek PCIe GbE Family Controller #8	 (ISA) 0x00000068 (104)	Microsoft ACPI-Compliant System
 (ISA) 0x0000000B (11)	Standard NVM Express Controller	 (ISA) 0x00000069 (105)	Microsoft ACPI-Compliant System
 (ISA) 0x00000036 (54)	Microsoft ACPI-Compliant System	 (ISA) 0x0000006A (106)	Microsoft ACPI-Compliant System
 (ISA) 0x00000037 (55)	Microsoft ACPI-Compliant System	 (ISA) 0x0000006B (107)	Microsoft ACPI-Compliant System
 (ISA) 0x00000038 (56)	Microsoft ACPI-Compliant System	 (ISA) 0x0000006C (108)	Microsoft ACPI-Compliant System
 (ISA) 0x00000039 (57)	Microsoft ACPI-Compliant System	 (ISA) 0x0000006D (109)	Microsoft ACPI-Compliant System
 (ISA) 0x0000003A (58)	Microsoft ACPI-Compliant System	 (ISA) 0x0000006E (110)	Microsoft ACPI-Compliant System
 (ISA) 0x0000003B (59)	Microsoft ACPI-Compliant System	 (ISA) 0x0000006F (111)	Microsoft ACPI-Compliant System
 (ISA) 0x0000003C (60)	Microsoft ACPI-Compliant System	 (ISA) 0x00000070 (112)	Microsoft ACPI-Compliant System
 (ISA) 0x0000003D (61)	Microsoft ACPI-Compliant System	 (ISA) 0x00000071 (113)	Microsoft ACPI-Compliant System
 (ISA) 0x0000003E (62)	Microsoft ACPI-Compliant System	 (ISA) 0x00000072 (114)	Microsoft ACPI-Compliant System
 (ISA) 0x0000003F (63)	Microsoft ACPI-Compliant System	 (ISA) 0x00000073 (115)	Microsoft ACPI-Compliant System
 (ISA) 0x00000040 (64)	Microsoft ACPI-Compliant System	 (ISA) 0x00000074 (116)	Microsoft ACPI-Compliant System
 (ISA) 0x00000041 (65)	Microsoft ACPI-Compliant System	 (ISA) 0x00000075 (117)	Microsoft ACPI-Compliant System
 (ISA) 0x00000042 (66)	Microsoft ACPI-Compliant System	 (ISA) 0x00000076 (118)	Microsoft ACPI-Compliant System
 (ISA) 0x00000043 (67)	Microsoft ACPI-Compliant System	 (ISA) 0x00000077 (119)	Microsoft ACPI-Compliant System
 (ISA) 0x00000044 (68)	Microsoft ACPI-Compliant System	 (ISA) 0x00000078 (120)	Microsoft ACPI-Compliant System
 (ISA) 0x00000045 (69)	Microsoft ACPI-Compliant System	 (ISA) 0x00000079 (121)	Microsoft ACPI-Compliant System
 (ISA) 0x00000046 (70)	Microsoft ACPI-Compliant System	 (ISA) 0x0000007A (122)	Microsoft ACPI-Compliant System
 (ISA) 0x00000047 (71)	Microsoft ACPI-Compliant System	 (ISA) 0x0000007B (123)	Microsoft ACPI-Compliant System
 (ISA) 0x00000048 (72)	Microsoft ACPI-Compliant System	 (ISA) 0x0000007C (124)	Microsoft ACPI-Compliant System
 (ISA) 0x00000049 (73)	Microsoft ACPI-Compliant System	 (ISA) 0x0000007D (125)	Microsoft ACPI-Compliant System
 (ISA) 0x0000004A (74)	Microsoft ACPI-Compliant System	 (ISA) 0x0000007E (126)	Microsoft ACPI-Compliant System
 (ISA) 0x0000004B (75)	Microsoft ACPI-Compliant System	 (ISA) 0x0000007F (127)	Microsoft ACPI-Compliant System
 (ISA) 0x0000004C (76)	Microsoft ACPI-Compliant System	 (ISA) 0x00000080 (128)	Microsoft ACPI-Compliant System
 (ISA) 0x0000004D (77)	Microsoft ACPI-Compliant System	 (ISA) 0x00000081 (129)	Microsoft ACPI-Compliant System
 (ISA) 0x0000004E (78)	Microsoft ACPI-Compliant System	 (ISA) 0x00000082 (130)	Microsoft ACPI-Compliant System
 (ISA) 0x0000004F (79)	Microsoft ACPI-Compliant System	 (ISA) 0x00000083 (131)	Microsoft ACPI-Compliant System
 (ISA) 0x00000050 (80)	Microsoft ACPI-Compliant System	 (ISA) 0x00000084 (132)	Microsoft ACPI-Compliant System
 (ISA) 0x00000051 (81)	Microsoft ACPI-Compliant System	 (ISA) 0x00000085 (133)	Microsoft ACPI-Compliant System
 (ISA) 0x00000052 (82)	Microsoft ACPI-Compliant System	 (ISA) 0x00000086 (134)	Microsoft ACPI-Compliant System







	(ISA) 0x000001F2 (498)	Microsoft ACPI-Compliant System
	(ISA) 0x000001F3 (499)	Microsoft ACPI-Compliant System
	(ISA) 0x000001F4 (500)	Microsoft ACPI-Compliant System
	(ISA) 0x000001F5 (501)	Microsoft ACPI-Compliant System
	(ISA) 0x000001F6 (502)	Microsoft ACPI-Compliant System
	(ISA) 0x000001F7 (503)	Microsoft ACPI-Compliant System
	(ISA) 0x000001F8 (504)	Microsoft ACPI-Compliant System
	(ISA) 0x000001F9 (505)	Microsoft ACPI-Compliant System
	(ISA) 0x000001FA (506)	Microsoft ACPI-Compliant System
	(ISA) 0x000001FB (507)	Microsoft ACPI-Compliant System
	(ISA) 0x000001FC (508)	Microsoft ACPI-Compliant System
	(ISA) 0x000001FD (509)	Microsoft ACPI-Compliant System
	(ISA) 0x000001FE (510)	Microsoft ACPI-Compliant System
	(ISA) 0x000001FF (511)	Microsoft ACPI-Compliant System
	(PCI) 0x00000035 (53)	AMD Audio CoProcessor
	(PCI) 0x00000035 (53)	High Definition Audio Controller
	(PCI) 0x00000036 (54)	High Definition Audio Controller
	(PCI) 0x00000037 (55)	AMD SFH KMDf I2C
	(PCI) 0xFFFFFDF (-33)	Realtek PCIe GbE Family Controller #3
	(PCI) 0xFFFFFE0 (-32)	Realtek PCIe GbE Family Controller #2
	(PCI) 0xFFFFFE1 (-31)	Realtek PCIe GbE Family Controller #4
	(PCI) 0xFFFFFE2 (-30)	Realtek PCIe GbE Family Controller
	(PCI) 0xFFFFFE3 (-29)	AMD Radeon(TM) Vega 8 Graphics
	(PCI) 0xFFFFFE4 (-28)	AMD Radeon(TM) Vega 8 Graphics
	(PCI) 0xFFFFFE5 (-27)	AMD Radeon(TM) Vega 8 Graphics
	(PCI) 0xFFFFFE6 (-26)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFE7 (-25)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFE8 (-24)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFE9 (-23)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFEA (-22)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFEB (-21)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFEC (-20)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFED (-19)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFEE (-18)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFEF (-17)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFFF0 (-16)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFFF1 (-15)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFFF2 (-14)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFFF3 (-13)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFFF4 (-12)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFFF5 (-11)	AMD USB 3.10 eXtensible Host Controller - 1.10 (Microsoft)
	(PCI) 0xFFFFFFF6 (-10)	AMD PSP 10.0 Device
	(PCI) 0xFFFFFFF7 (-9)	AMD PSP 10.0 Device
	(PCI) 0xFFFFFFF8 (-8)	Standard SATA AHCI Controller
	(PCI) 0xFFFFFFF9 (-7)	PCI Express Root Port
	(PCI) 0xFFFFFFFA (-6)	PCI Express Root Port
	(PCI) 0xFFFFFFFB (-5)	PCI Express Root Port
	(PCI) 0xFFFFFFFC (-4)	PCI Express Root Port
	(PCI) 0xFFFFFFFD (-3)	PCI Express Root Port
	(PCI) 0xFFFFFFE (-2)	PCI Express Root Port

## 3.6 Memory Map

The memory mapping list is shown as follows:

- ▼  Memory
  - >  [00000000000A0000 - 00000000000BFFFF] PCI Express Root Complex
  -  [00000000000C0000 - 00000000000DFFFF] PCI Express Root Complex
  - >  [00000000E0000000 - 00000000FEBFFFFF] PCI Express Root Complex
    -  [00000000FEC00000 - 00000000FEC00FFF] Motherboard resources
    -  [00000000FEC01000 - 00000000FEC01FFF] Motherboard resources
    -  [00000000FEC10000 - 00000000FEC10FFF] Motherboard resources
    -  [00000000FED00000 - 00000000FED003FF] High precision event timer
    -  [00000000FED40000 - 00000000FED44FFF] Trusted Platform Module 2.0
  - >  [00000000FED80000 - 00000000FED8FFFF] Motherboard resources
    -  [00000000FEDC0000 - 00000000FEDC0FFF] Motherboard resources
    -  [00000000FEDC7000 - 00000000FEDC7FFF] AMD UART Controller
    -  [00000000FEDC8000 - 00000000FEDC8FFF] AMD UART Controller
    -  [00000000FEDC9000 - 00000000FEDC9FFF] AMD UART Controller
    -  [00000000FEDCA000 - 00000000FEDCAFFF] AMD UART Controller
  - >  [00000000FEE00000 - 00000000FFFFFFF] PCI Express Root Complex

---

# Chapter 4

## AMI BIOS Setup Utility

The AMI UEFI BIOS provides users with a built-in setup program to modify basic system configuration. All configured parameters are stored in a flash chip to save the setup information whenever the power is turned off. This chapter provides users with detailed description about how to set up basic system configuration through the AMI BIOS setup utility.

### 4.1 Starting

To enter the setup screens, follow the steps below:

1. Turn on the computer and press the <Del> key immediately.
2. After you press the <Del> key, the main BIOS setup menu displays. You can access the other setup screens from the main BIOS setup menu, such as the Advanced and Chipset menus.



**Note**

*If your computer cannot boot after making and saving system changes with BIOS setup, you can restore BIOS optimal defaults by setting SW1 (see section 2.4.2).*

It is strongly recommended that you should avoid changing the chipset's defaults. Both AMI and your system manufacturer have carefully set up these defaults that provide the best performance and reliability.

### 4.2 Navigation Keys

The BIOS setup/utility uses a key-based navigation system called hot keys. Most of the BIOS setup utility hot keys can be used at any time during the setup navigation process. These keys include <F1>, <F2>, <Enter>, <ESC>, <Arrow> keys, and so on.



**Note**

*Some of the navigation keys differ from one screen to another.*

Hot Keys	Description
→← Left/Right	The Left and Right <Arrow> keys allow you to select a setup screen.
↑↓ Up/Down	The Up and Down <Arrow> keys allow you to select a setup screen or sub-screen.
+– Plus/Minus	The Plus and Minus <Arrow> keys allow you to change the field value of a particular setup item.
Tab	The <Tab> key allows you to select setup fields.
F1	The <F1> key allows you to display the General Help screen.
F2	The <F2> key allows you to Load Previous Values.
F3	The <F3> key allows you to Load Optimized Defaults.
F4	The <F4> key allows you to save any changes you have made and exit Setup. Press the <F4> key to save your changes.
Esc	The <Esc> key allows you to discard any changes you have made and exit the Setup. Press the <Esc> key to exit the setup without saving your changes.
Enter	The <Enter> key allows you to display or change the setup option listed for a particular setup item. The <Enter> key can also allow you to display the setup sub- screens.

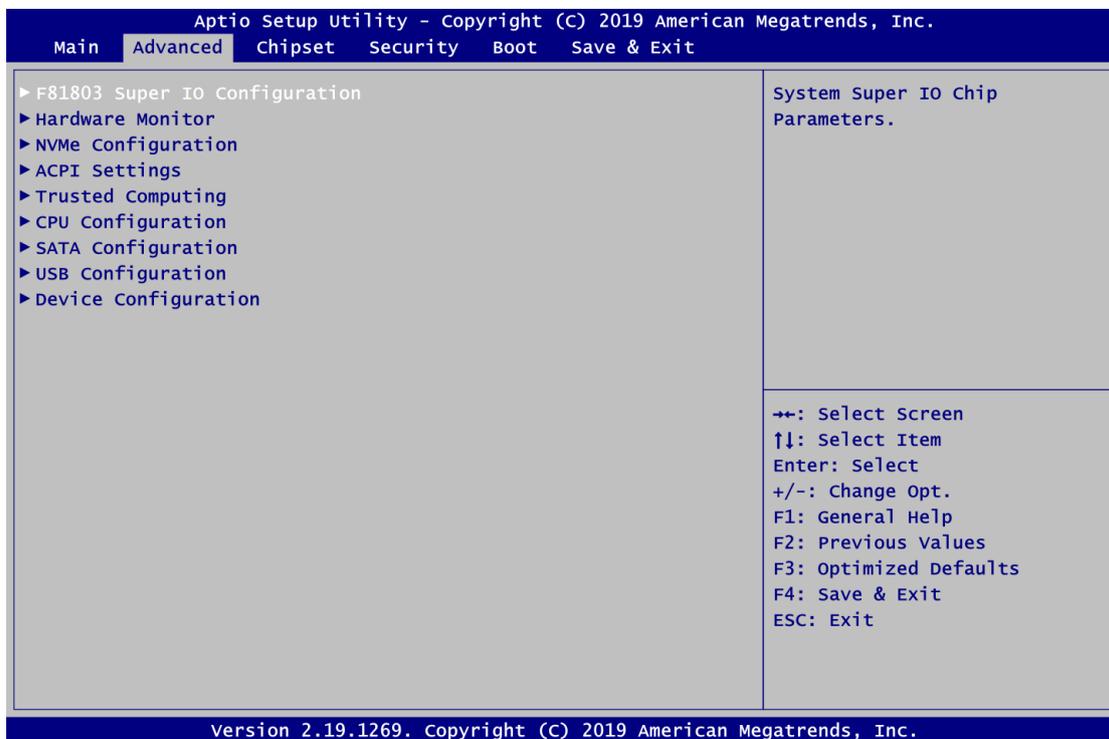


## 4.4 Advanced Menu

The Advanced menu also allows users to set configuration of the CPU and other system devices. You can select any of the items in the left frame of the screen to go to the sub menus:

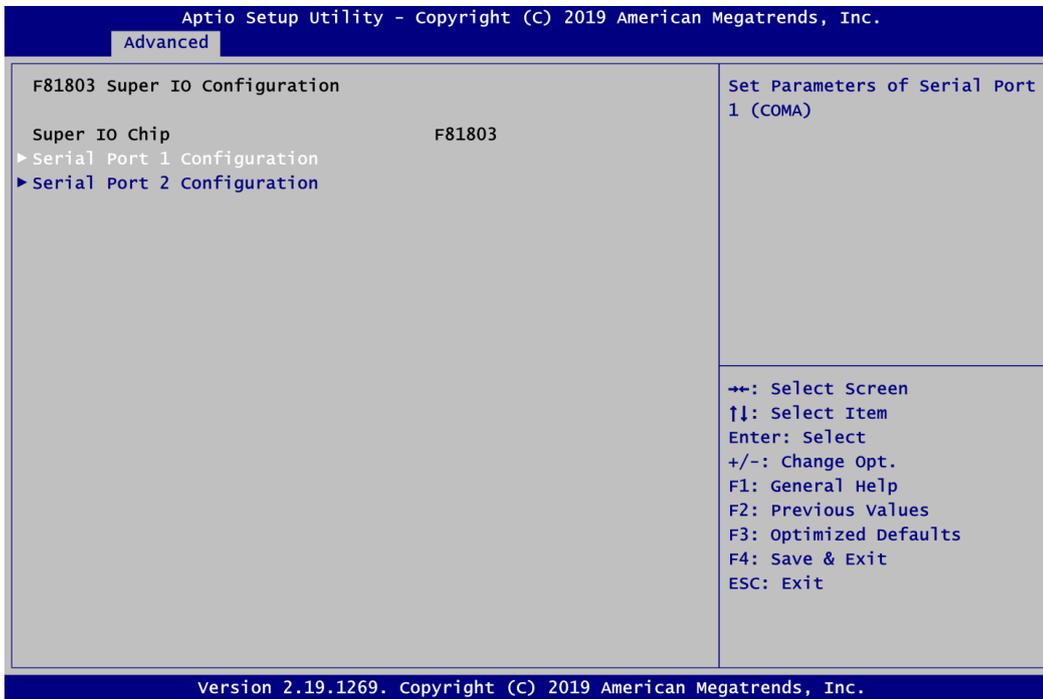
- ▶ F81803 Super IO Configuration
- ▶ Hardware Monitor
- ▶ NVMe Configuration (Optional)
- ▶ ACPI Settings
- ▶ Trusted Computing
- ▶ CPU Configuration
- ▶ SATA Configuration
- ▶ USB Configuration
- ▶ Device Configuration

For items marked with “▶”, please press <Enter> for more options.



- **F81803 Super IO Configuration**

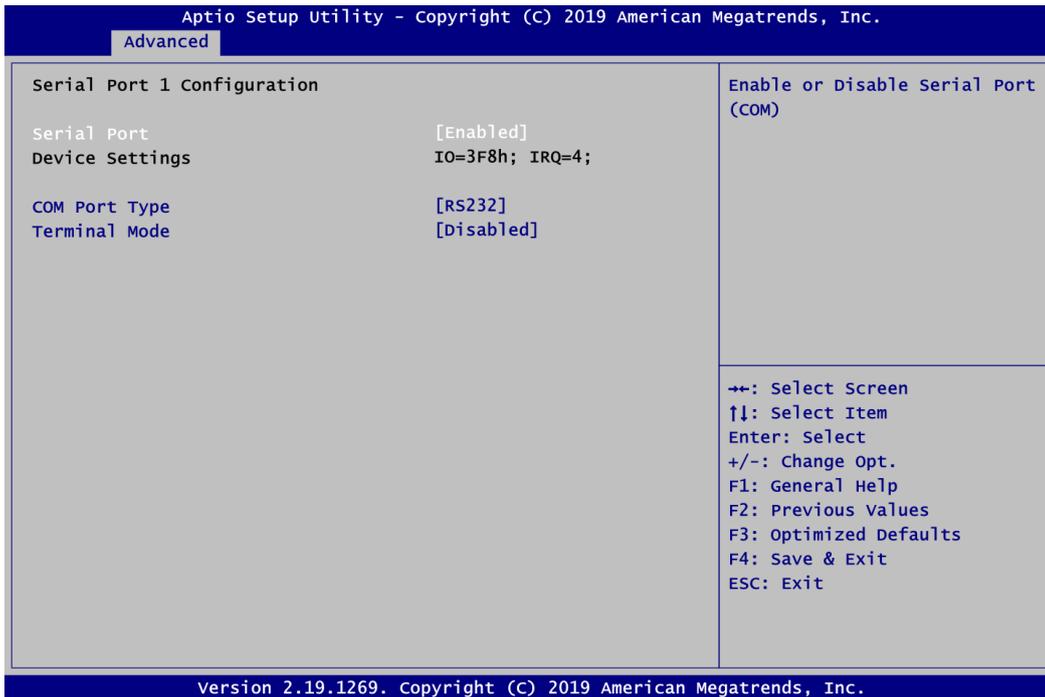
You can use this screen to select options for serial port configuration, and change the value of the selected option. A description of the selected item appears on the right side of the screen. For items marked with "▶", please press <Enter> for more options.



### Serial Port 1~2 Configuration

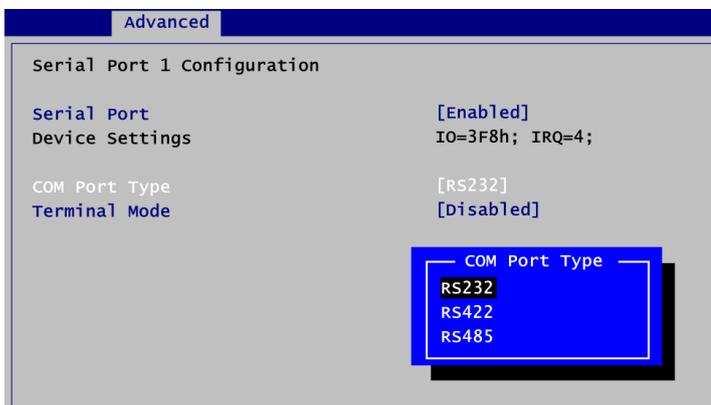
Use these items to set parameters related to serial port 1~2.

- Serial Port 1 Configuration



**Serial Port**

Enable or disable serial port 1. The optimal setting for base I/O address is 3F8h and for interrupt request address is IRQ4.



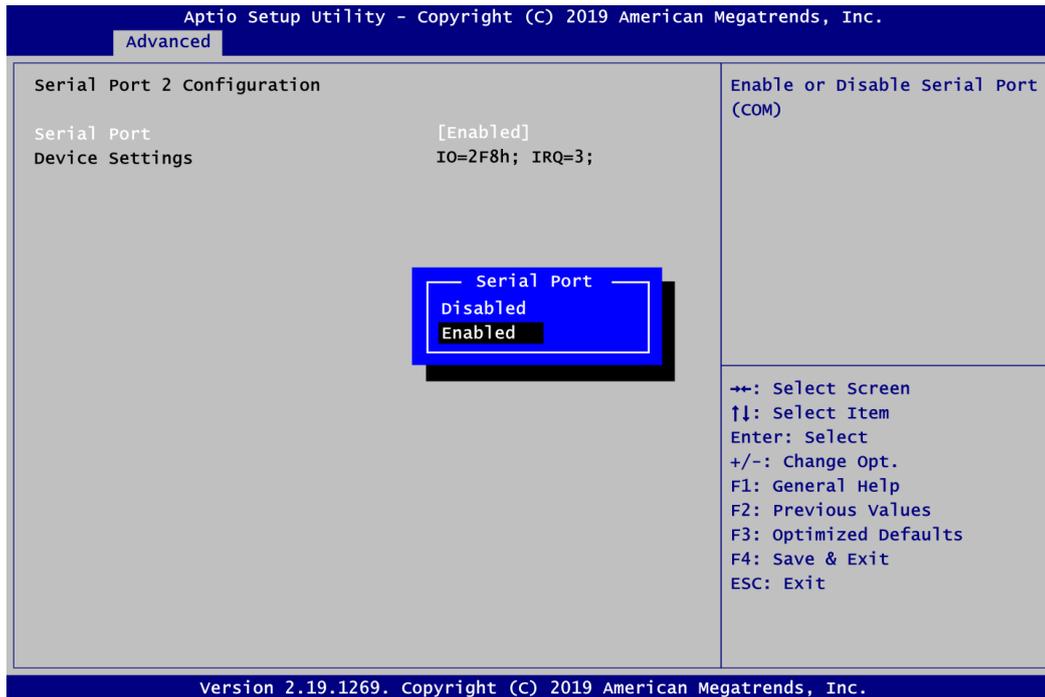
**COM Port Type**

Use this item to set RS-232/422/485 communication mode.

**Terminal Mode**

Enable terminal mode to enable the RS-422/485 termination resistor to enhance the signal.

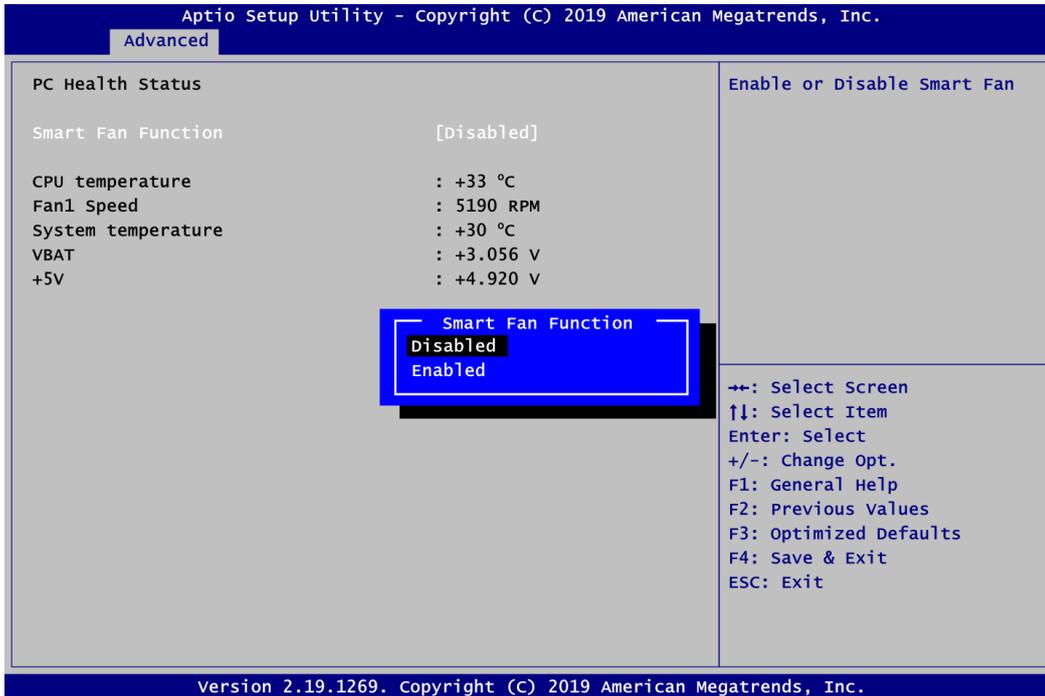
- **Serial Port 2 Configuration**

**Serial Port**

Enable or disable serial port 2. The optimal setting for base I/O address is 2F8h and for interrupt request address is IRQ3.

● **Hardware Monitor**

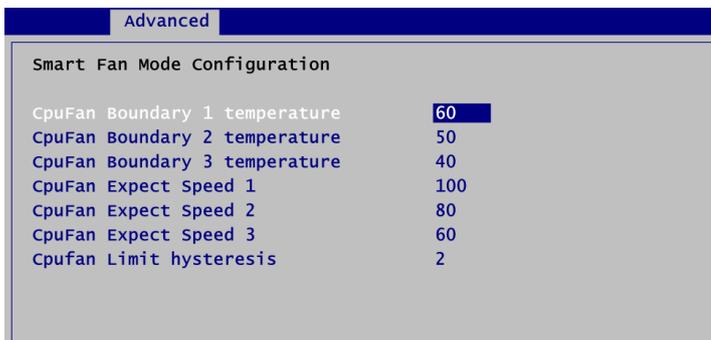
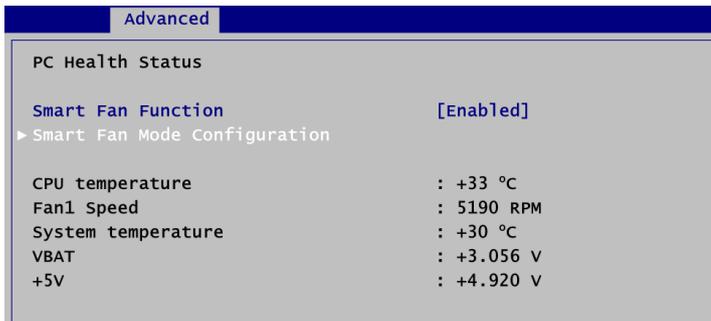
This screen monitors hardware health status.



This screen displays the temperature of system and CPU, fan speed in RPM and system voltages (VBAT and +5V).

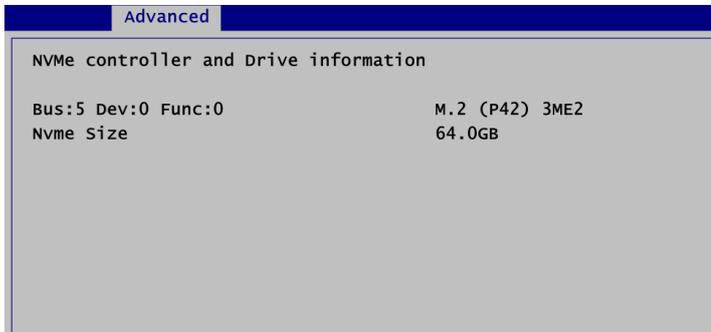
**Smart Fan Function**

Enable or disable Smart Fan control function. Once enabled, you will be able to go further for Smart Fan Mode Configuration, see image below.

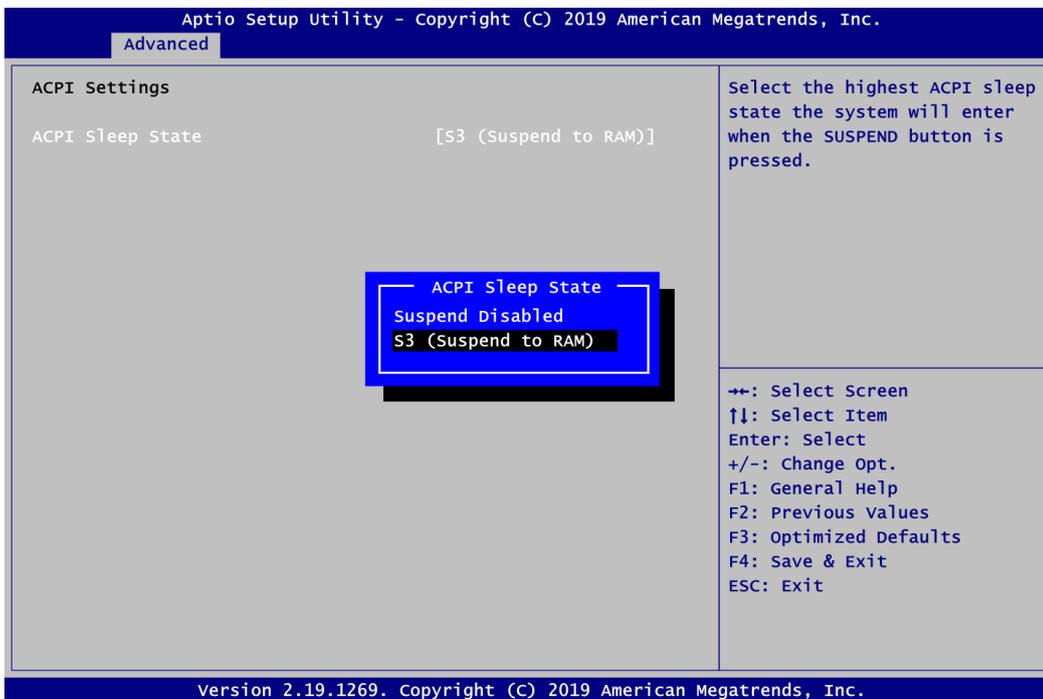


- **NVMe Configuration (Optional)**

If M.2 NVMe card is installed in M.2 2242/3042 Key B Connector (CN10) (see section 2.5.8) with BOM option change, you will also need customized BIOS to show and enable NVMe Configuration. The following screen displays NVMe (Non-Volatile Memory Express) controller and drive information.



- **ACPI Settings**

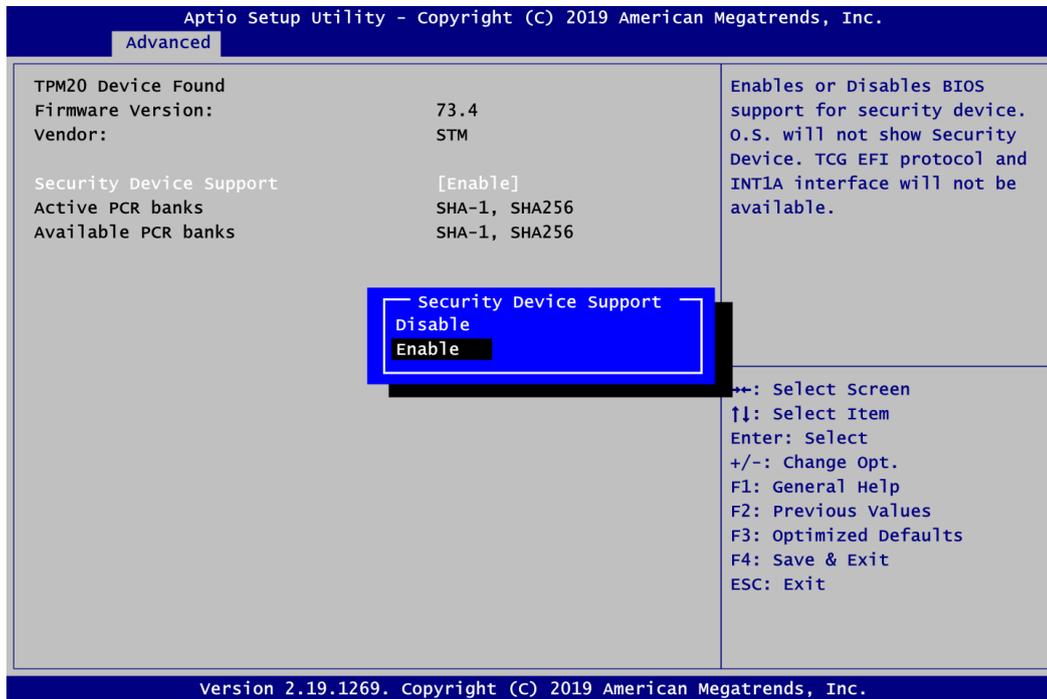


### ACPI Sleep State

Select the ACPI (Advanced Configuration and Power Interface) sleep state. Configuration options are Suspend Disabled and S3 (Suspend to RAM). When S3 (Suspend to RAM) option is selected, the system will enter after suspend button is pressed.

- **Trusted Computing**

You can use this screen for TPM (Trusted Platform Module) configuration. It also shows current TPM status information.

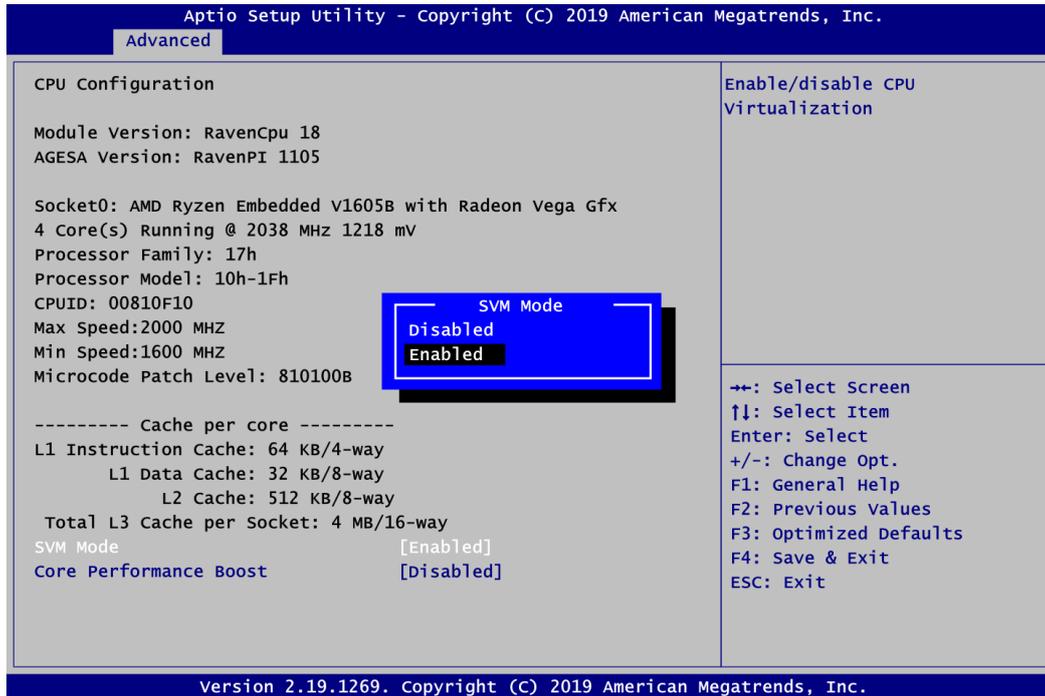


**Security Device Support**

Enable or disable BIOS support for security device.

- **CPU Configuration**

This screen shows the CPU Configuration.



### SVM Mode

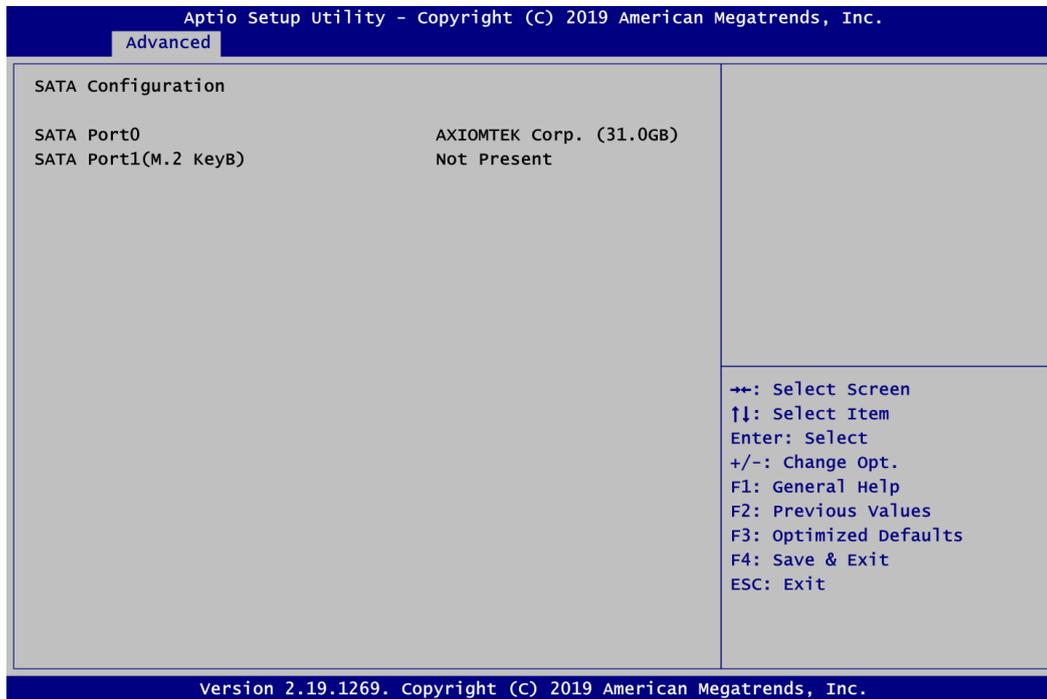
Enable or disable SVM (Secure Virtual Machine) mode. Once enabled, you will be able to install a virtual machine on your system.

### Core Performance Boost

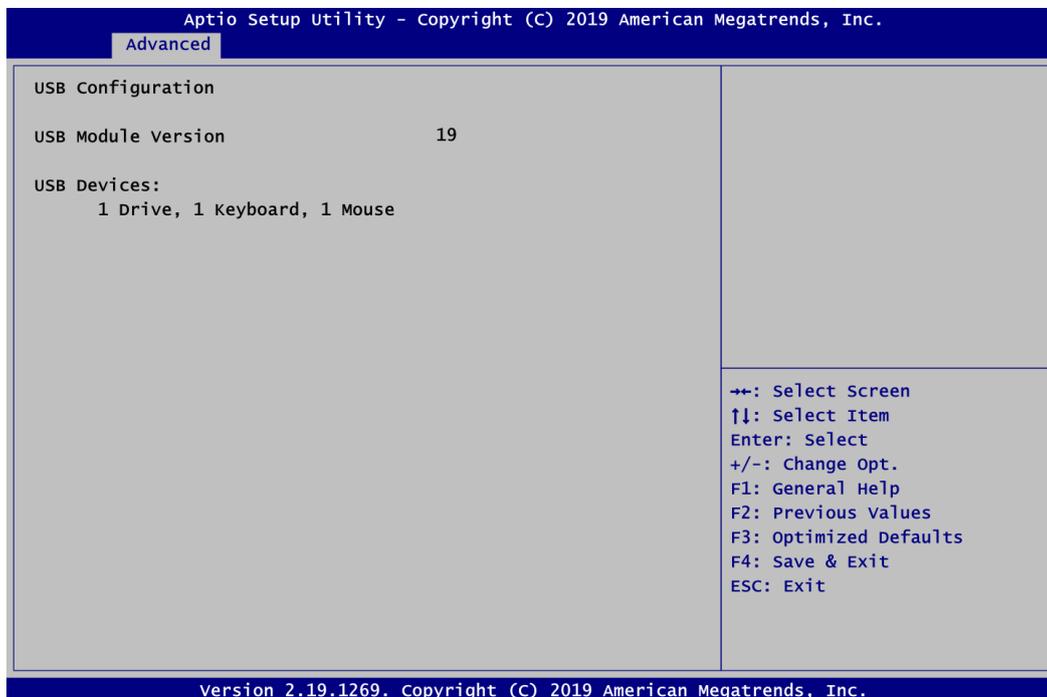
If enabled, the CPU can be boosted up to its maximum clock speed when needed.

- **SATA Configuration**

During system boot up, BIOS automatically detects the presence of SATA devices. In the SATA Configuration menu, you can see the hardware currently installed in SATA ports.



- **USB Configuration**

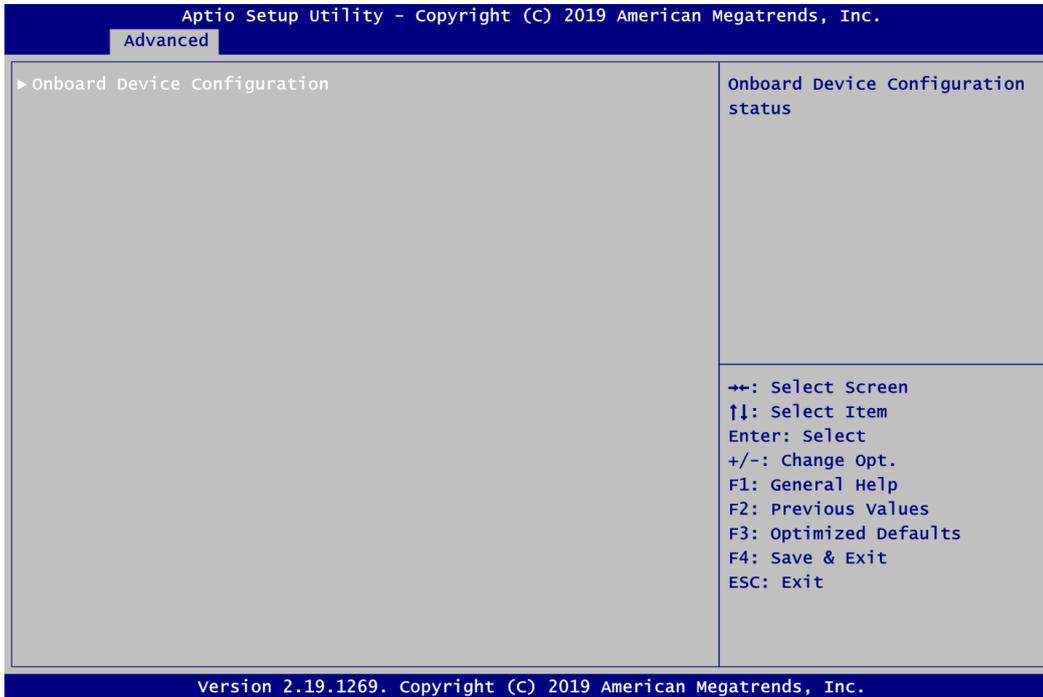


**USB Devices**

Display all detected USB devices.

- **Onboard Device Configuration.**

A description of the selected item appears on the right side of the screen. For items marked with “▶”, please press <Enter> for more options.

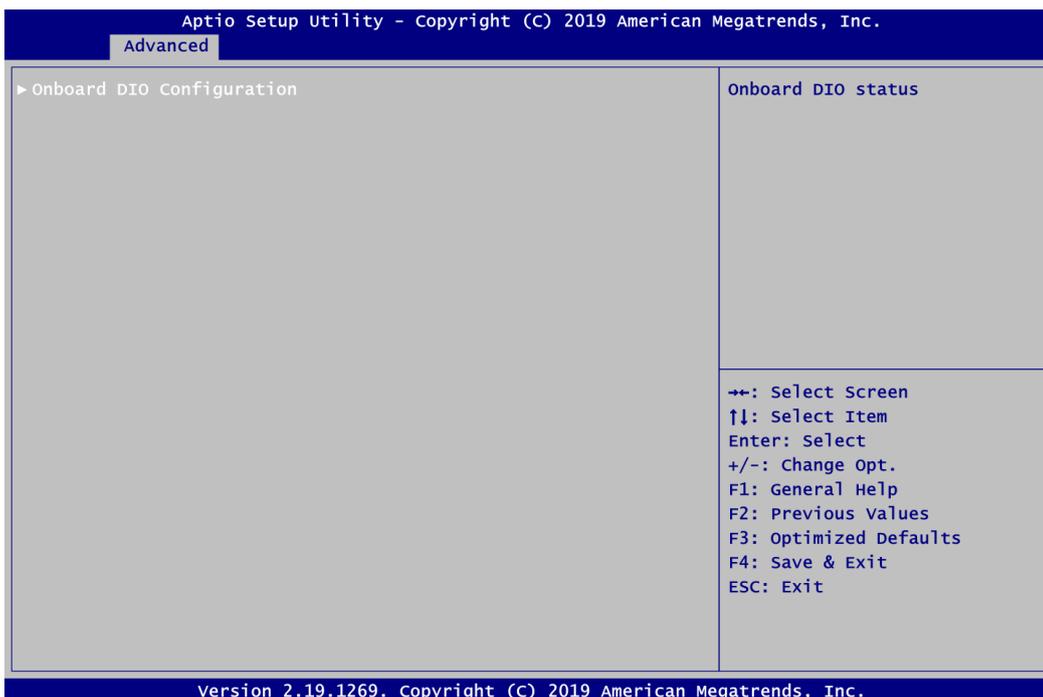


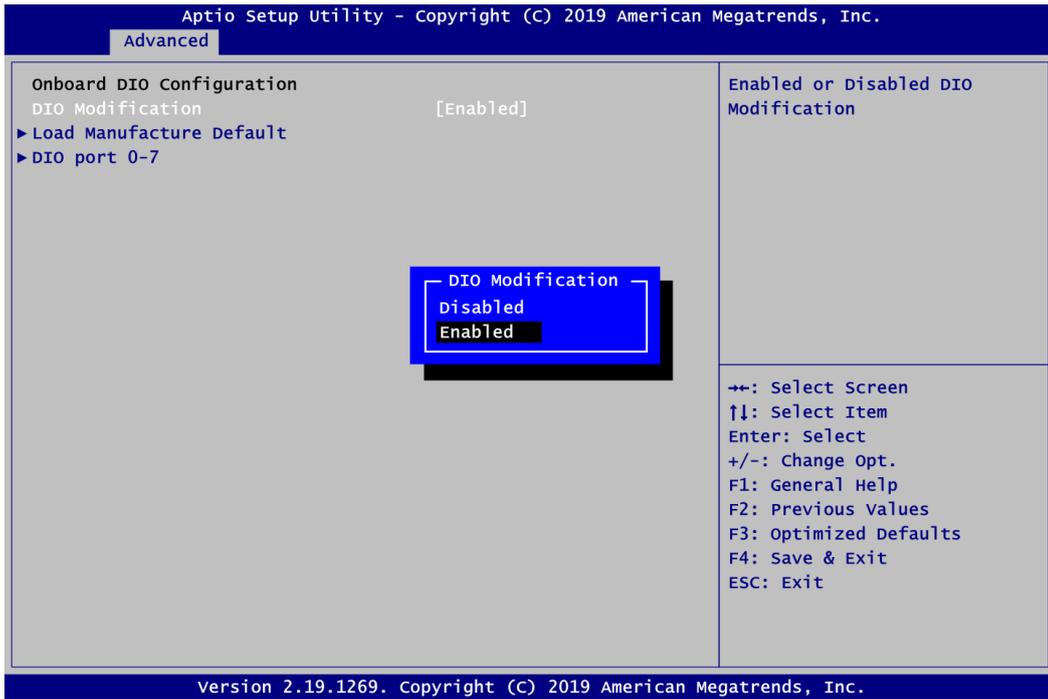
### Onboard Device Configuration

Use this option to configure onboard device (e.g., Digital I/O setting).

- **Onboard DIO Configuration**

You can use this screen to select options for the 8-bit Digital I/O Configuration. A description of the selected item appears on the right side of the screen. For items marked with “▶”, please press <Enter> for more options.





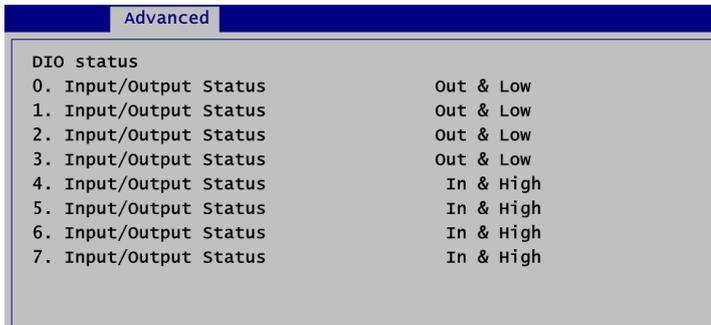
**DIO Modification**

Enable or disable digital I/O modification. The default is Disabled.

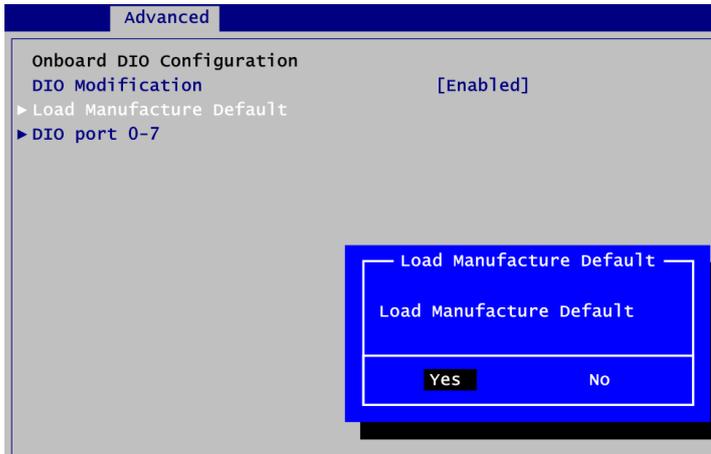
**DIO port 0-7**

Select this option to open DIO status sub-screen.

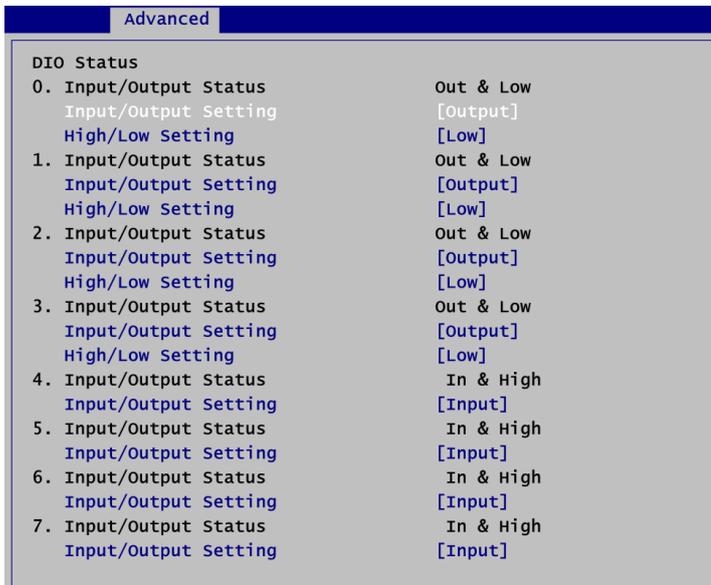
If DIO Modification is disabled, you are not allowed to change inputs/outputs setting. The DIO status sub-screen is as follows:



If DIO Modification is enabled, you can load manufacture default and access to the DIO status sub-screen to change input/output setting, see image below.



The DIO status sub-screen is as follows:

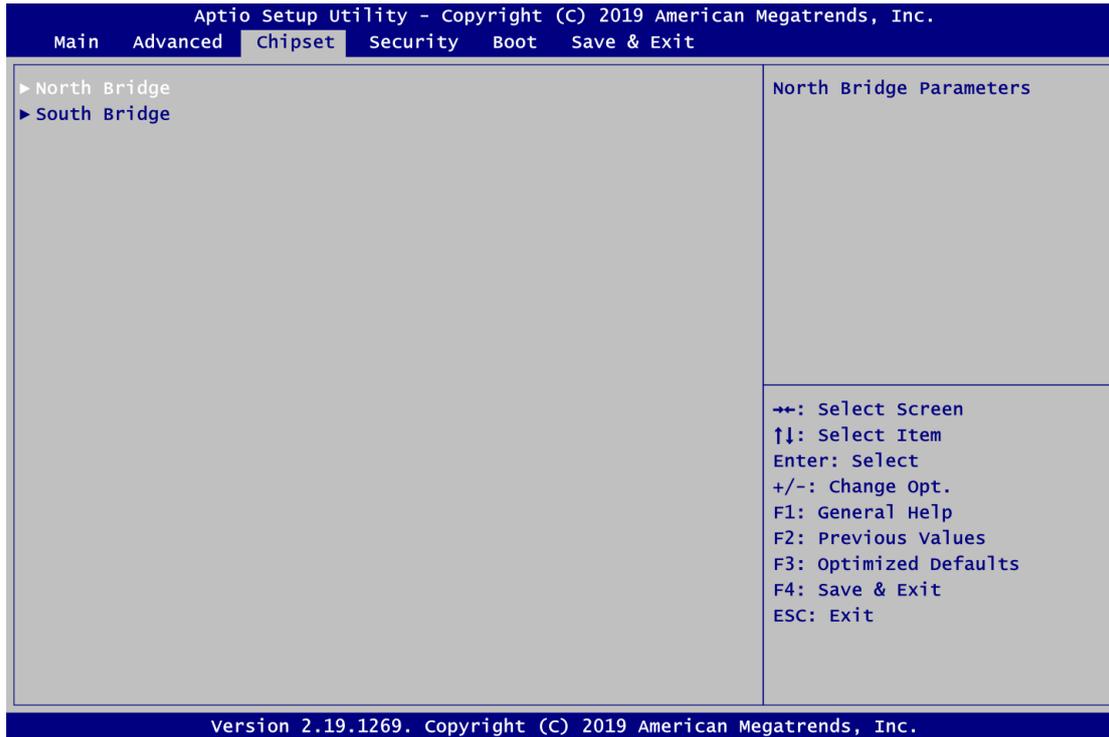


## 4.5 Chipset Menu

The Chipset menu allows users to change the advanced chipset settings. You can select any of the items in the left frame of the screen to go to the sub menus:

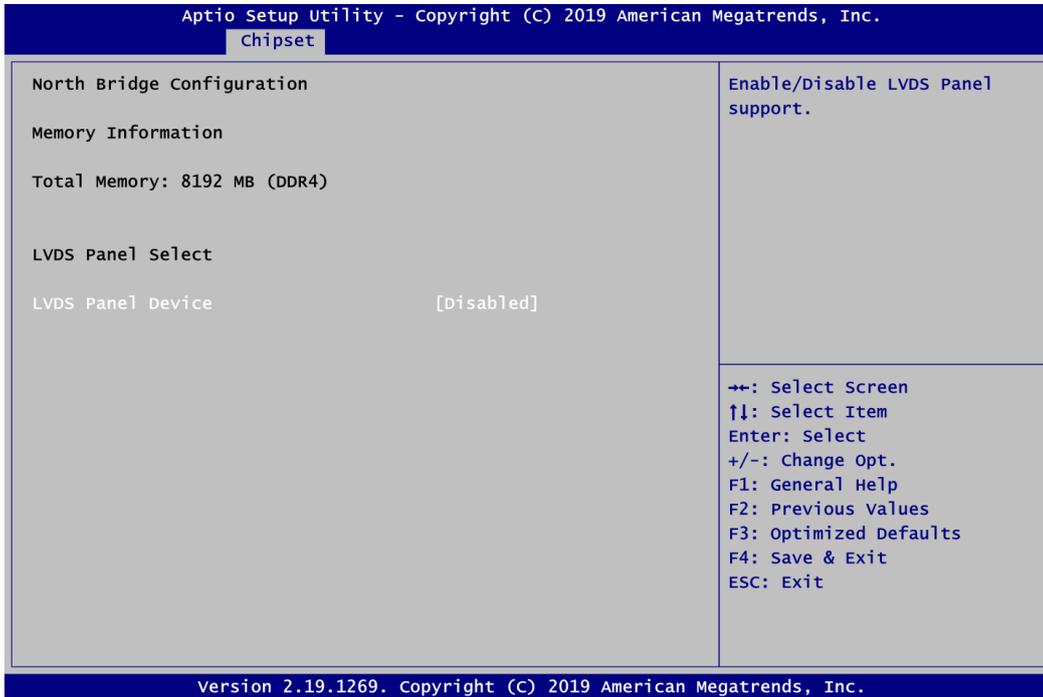
- ▶ North Bridge
- ▶ South Bridge

For items marked with “▶”, please press <Enter> for more options.



- **North Bridge**

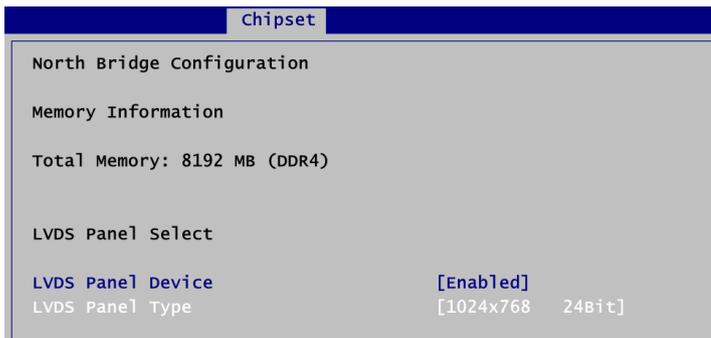
This screen displays system memory information. And it also allows users to configure parameters of North Bridge chipset.



#### LVDS Panel Device

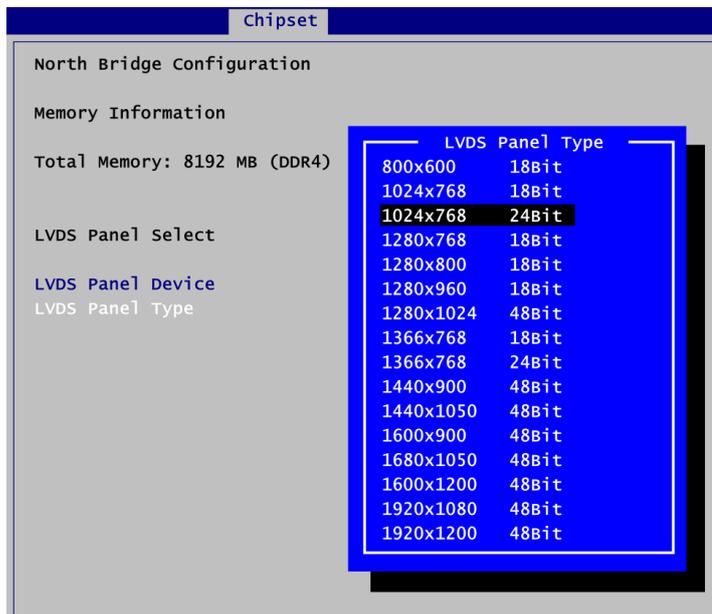
Enable or disable LVDS panel support. Once enabled, you will be able to go further for LVDS Panel Type setting, see image below.

Under BIOS setup or Shell/DOS mode, only one display, either DisplayPort or HDMI or LVDS, can be displayed. However, when enable this item, the LVDS panel device is the only one display can be showed.



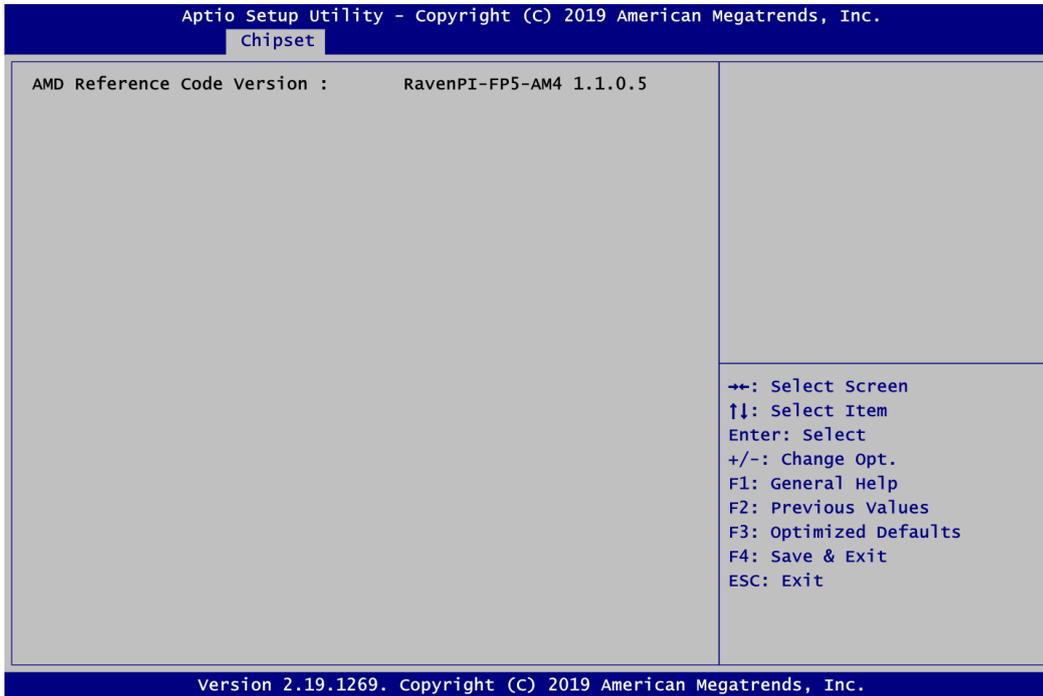
### LVDS Panel Type

Select the appropriate LVDS panel resolution, see image below.



- **South Bridge**

This screen shows the information of South Bridge chipset.



## 4.6 Security Menu

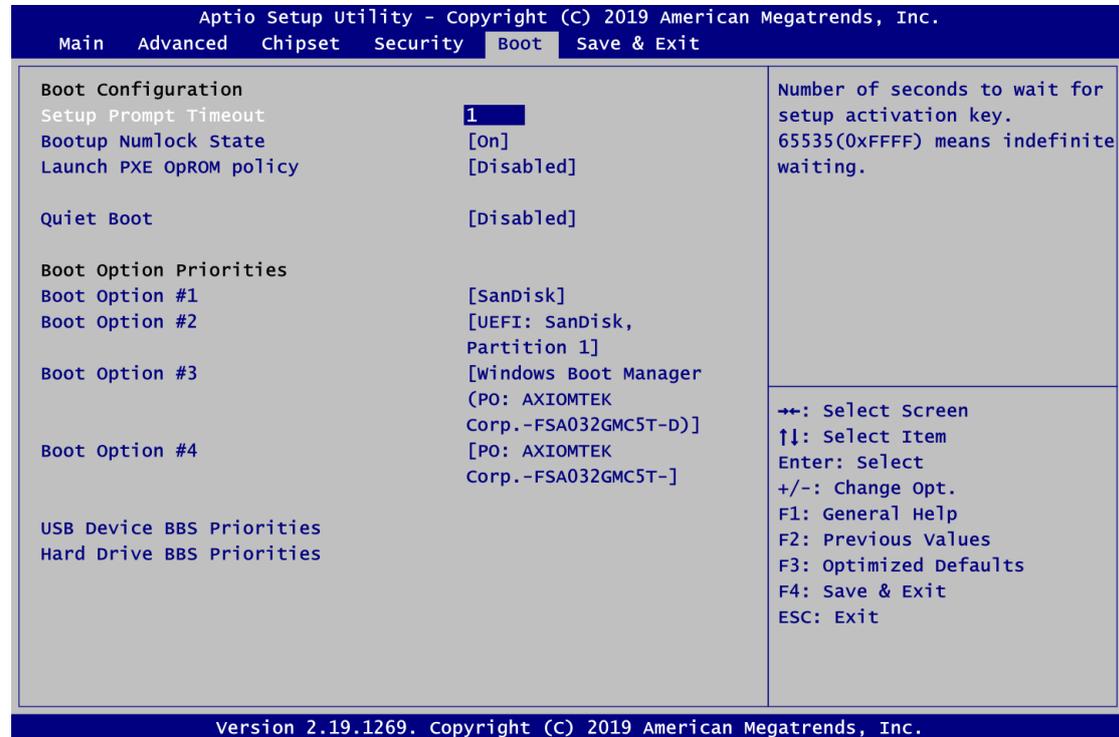
The Security menu allows users to change the security settings for the system.



- **Administrator Password.**  
Set administrator password.
- **User Password**  
Set user password.

## 4.7 Boot Menu

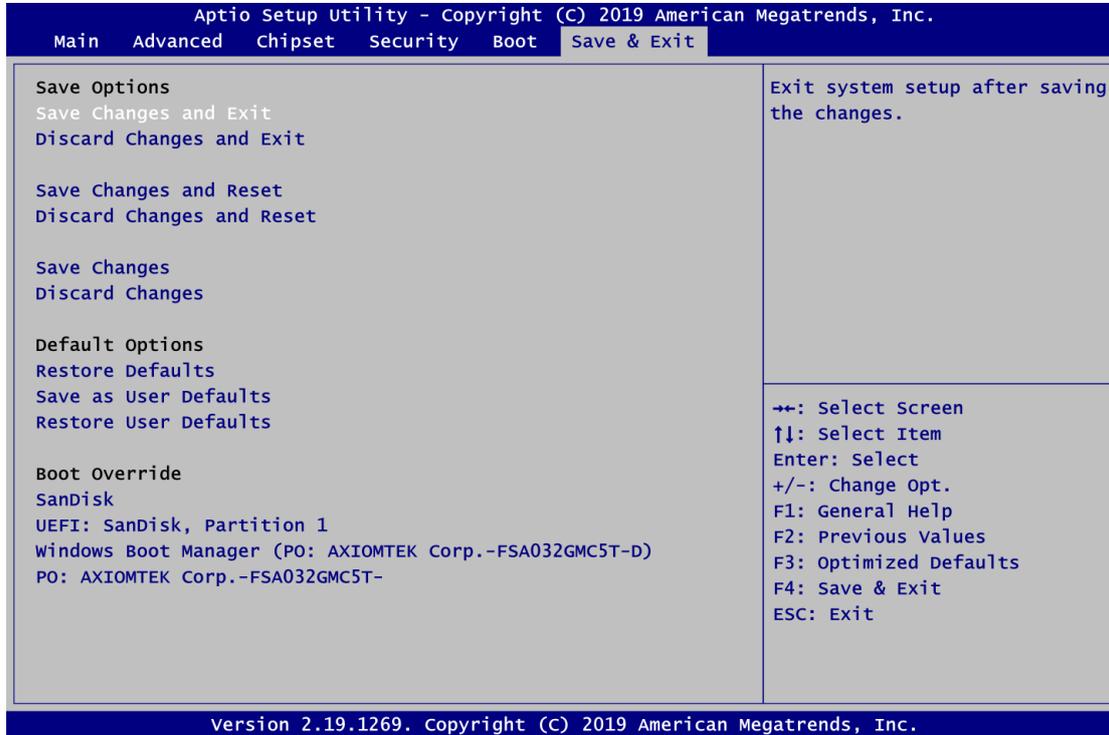
The Boot menu allows users to change boot options of the system.



- Setup Prompt Timeout**  
 Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
- Bootup NumLock State**  
 Use this item to select the power-on state for the keyboard NumLock.
- Launch PXE OpROM policy**  
 Use this item to enable or disable the boot ROM function of the onboard LAN chip when the system boots up.
- Quiet Boot**  
 Select to display either POST output messages or a splash screen during boot-up.
- Boot Option Priorities [Boot Option #1, ...]**  
 These are settings for boot priority. Specify the boot device priority sequence from the available devices.
- USB Device/Hard Drive BBS Priorities**  
 Set the boot order of the specific devices in this group. This option appears only if at least one device of this group is detected.

## 4.8 Save & Exit Menu

The Save & Exit menu allows users to load your system configuration with optimal or fail-safe default values.



- Save Changes and Exit**  
 When you have completed the system configuration changes, select this option to leave Setup and return to Main Menu. Select Save Changes and Exit from the Save & Exit menu and press <Enter>. Select Yes to save changes and exit.
- Discard Changes and Exit**  
 Select this option to quit Setup without making any permanent changes to the system configuration and return to Main Menu. Select Discard Changes and Exit from the Save & Exit menu and press <Enter>. Select Yes to discard changes and exit.
- Save Changes and Reset**  
 When you have completed the system configuration changes, select this option to leave Setup and reboot the computer so the new system configuration parameters can take effect. Select Save Changes and Reset from the Save & Exit menu and press <Enter>. Select Yes to save changes and reset.
- Discard Changes and Reset**  
 Select this option to quit Setup without making any permanent changes to the system configuration and reboot the computer. Select Discard Changes and Reset from the Save & Exit menu and press <Enter>. Select Yes to discard changes and reset.
- Save Changes**  
 When you have completed the system configuration changes, select this option to save changes. Select Save Changes from the Save & Exit menu and press <Enter>. Select Yes to save changes.

- **Discard Changes**  
Select this option to quit Setup without making any permanent changes to the system configuration. Select Discard Changes from the Save & Exit menu and press <Enter>. Select Yes to discard changes.
- **Restore Defaults**  
It automatically sets all Setup options to a complete set of default settings when you select this option. Select Restore Defaults from the Save & Exit menu and press <Enter>.
- **Save as User Defaults**  
Select this option to save system configuration changes done so far as User Defaults. Select Save as User Defaults from the Save & Exit menu and press <Enter>.
- **Restore User Defaults**  
It automatically sets all Setup options to a complete set of User Defaults when you select this option. Select Restore User Defaults from the Save & Exit menu and press <Enter>.
- **Boot Override**  
Select a drive to immediately boot that device regardless of the current boot order.

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# Appendix A

## Watchdog Timer

### A.1 About Watchdog Timer

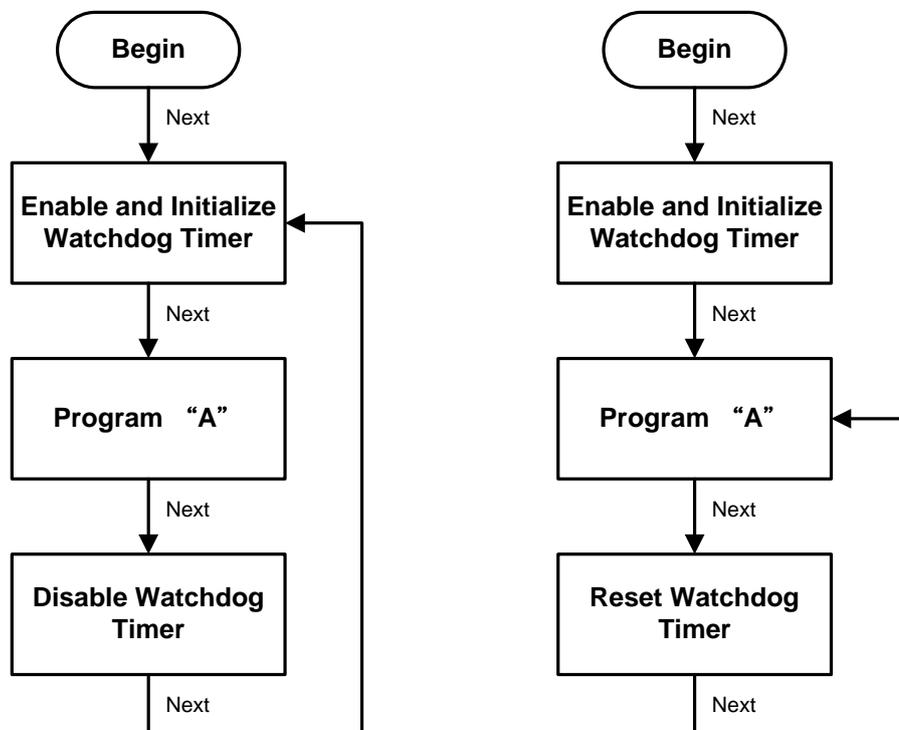
Software stability is major issue in most application. Some embedded systems are not watched by operator for 24 hours. It is usually too slow to wait for someone to reboot when computer hangs. The systems need to be able to reset automatically when things go wrong. The watchdog timer gives us solution.

The watchdog timer is a counter that triggers a system reset when it counts down to zero from a preset value. The software starts counter with an initial value and must reset it periodically. If the counter ever reaches zero which means the software has crashed, the system will reboot.

### A.2 How to Use Watchdog Timer

The I/O port base addresses of watchdog timer are 2E (hex) and 2F (hex). The 2E (hex) and 2F (hex) are address and data port respectively.

Assume that program A is put in a loop that must execute at least once every 10ms. Initialize watchdog timer with a value bigger than 10ms. If the software has no problems; watchdog timer will never expire because software will always restart the counter before it reaches zero.



## A.3 Sample Program

Assembly sample code :

```
;Enable WDT:
mov     dx,2Eh
mov     al,87             ;Un-lock super I/O
out     dx,al
out     dx,al

;Select Logic device:
mov     dx,2Eh
mov     al,07h
out     dx,al
mov     dx,2Fh
mov     al,07h
out     dx,al

;Enable WDT base address:
mov     dx,2Eh
mov     al,30h
out     dx,al
mov     dx,2Fh
mov     al,01h
out     dx,al

;Activate WDT:
mov     dx,2Eh
mov     al,0F0h
out     dx,al
mov     dx,2Fh
mov     al,80h
out     dx,al

;Set base timer :
mov     dx,2Eh
mov     al,0F6h
out     dx,al
mov     dx,2Fh
mov     al,Mh             ;M=00h,01h,...FFh (hex),value=0 to 255
out     dx,al             ;(see  Note below)

;Set Second or Minute :
mov     dx,2Eh
mov     al,0F5h
out     dx,al
mov     dx,2Fh
mov     al,Nh             ;N=71h or 79h(see  Note below)
out     dx,al
```

 **Note:**

If **N=71h**, the time base is set to second.

**M** = time value

00: Time-out disable

01: Time-out occurs after 1 second

02: Time-out occurs after 2 seconds

03: Time-out occurs after 3 seconds

.

.

FFh: Time-out occurs after 255 seconds

If **N**=79h, the time base is set to minute.

**M** = time value

00: Time-out disable

01: Time-out occurs after 1 minute

02: Time-out occurs after 2 minutes

03: Time-out occurs after 3 minutes

.

.

FFh: Time-out occurs after 255 minutes

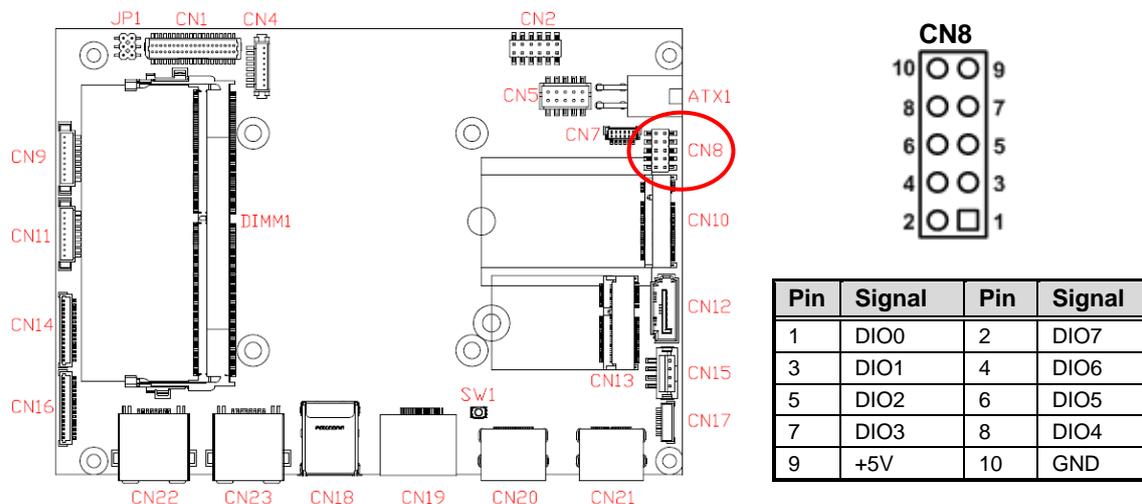
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# Appendix B

## Digital I/O

### B.1 About Digital I/O

The onboard GPIO or digital I/O has 8 bits. Each bit can be set to function as input or output by software programming. In default, all pins are pulled high with +5V level (according to main power). The BIOS default settings are 4 inputs and 4 outputs.



### B.2 Digital I/O Programming

- I<sup>2</sup>C to GPIO PCA9554PW GPIO.
- I<sup>2</sup>C address: 01000100.

#### Command byte

Command	Protocol	Function
0	Read byte	Input port register
1	Read/write byte	Output port register
2	Read/write byte	Polarity inversion register
3	Read/write byte	Configuration register

The command byte is the first byte to follow the address byte during a write transmission. It is used as a pointer to determine which of the following registers will be written or read.

**Register 0: Input port register.**

This register is a read-only port. It reflects the incoming logic levels of the pins, regardless of whether the pin is defined as an input or an output by Register 3. Writes to this register have no effect.

The default “X” is determined by the externally applied logic level, normally “1” when no external signal externally applied because of the internal pull-up resistors.

**Register 0 – Input port register bit description**

Bit	Symbol	Access	Value	Description
7	I7	Read only	X	Determined by externally applied logic level.
6	I6	Read only	X	
5	I5	Read only	X	
4	I4	Read only	X	
3	I3	Read only	X	
2	I2	Read only	X	
1	I1	Read only	X	
0	I0	Read only	X	

**Register 1: Output port register.**

This register reflects the outgoing logic levels of the pins defined as outputs by Register 3. Bit values in this register have no effect on pins defined as inputs. Reads from this register return the value that is in the flip-flop controlling the output selection, not the actual pin value.

**Register 1 – Output port register bit description**

Bit	Symbol	Access	Default Value	Description
7	O7	R	1	Reflects outgoing logic levels of pins defined as outputs by Register 3.
6	O6	R	1	
5	O5	R	1	
4	O4	R	1	
3	O3	R	1	
2	O2	R	1	
1	O1	R	1	
0	O0	R	1	

**Register 2: Polarity Inversion register.**

This register allows the user to invert the polarity of the Input port register data. If a bit in this register is set (written with “1”), the corresponding Input port data is inverted. If a bit in this register is cleared (written with “0”), the Input port data polarity is retained.

**Register 2 – Polarity inversion register bit description**

Bit	Symbol	Access	Default Value	Description
7	N7	R/W	0	Inverts polarity of Input port register data. 0 = Input port register data retained (default value). 1 = Input port register data inverted.
6	N6	R/W	0	
5	N5	R/W	0	
4	N4	R/W	0	
3	N3	R/W	0	
2	N2	R/W	0	
1	N1	R/W	0	
0	N0	R/W	0	

**Register 3: Configuration register.**

This register configures the directions of the I/O pins. If a bit in this register is set, the corresponding port pin is enabled as an input with high-impedance output driver. If a bit in this register is cleared, the corresponding port pin is enabled as an output. At reset, the I/Os are configured as inputs with a weak pull-up to  $V_{DD}$ .

**Register 3 – Configuration register bit description**

Bit	Symbol	Access	Default Value	Description
7	C7	R/W	1	Configures the directions of the I/O pins. 0 = Corresponding port pin enabled as an output. 1 = Corresponding port pin configured as input (default value).
6	C6	R/W	1	
5	C5	R/W	1	
4	C4	R/W	1	
3	C3	R/W	1	
2	C2	R/W	1	
1	C1	R/W	1	
0	C0	R/W	1	